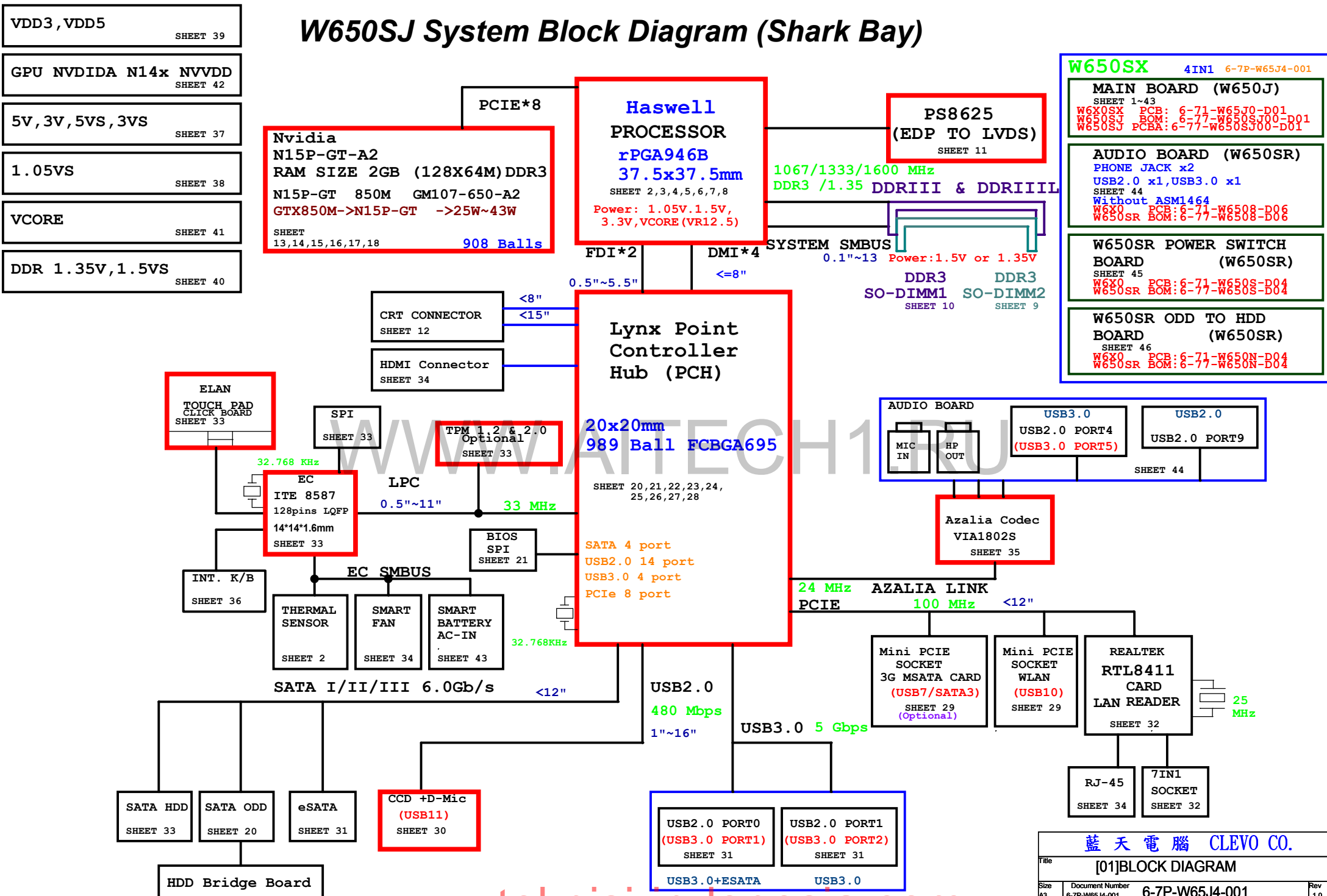
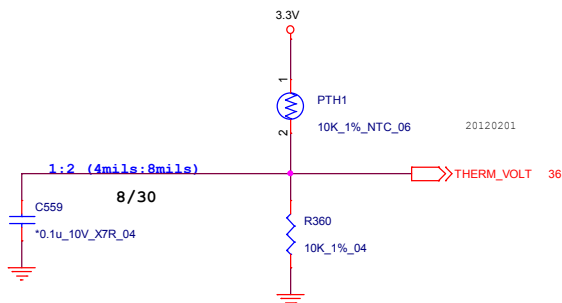


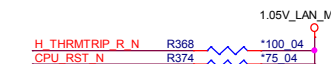
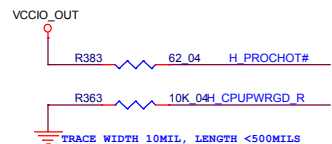
W650SJ System Block Diagram (Shark Bay)



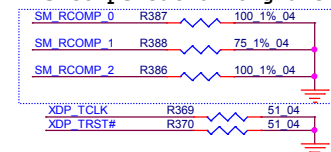
WWW.AITE-H1.RU



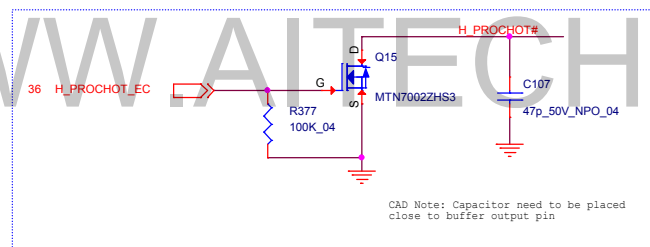
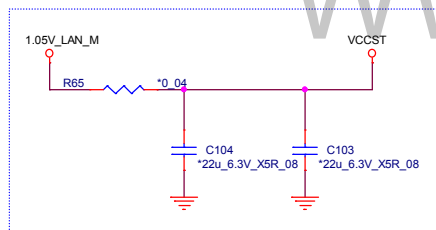
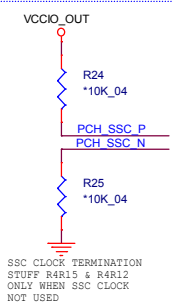
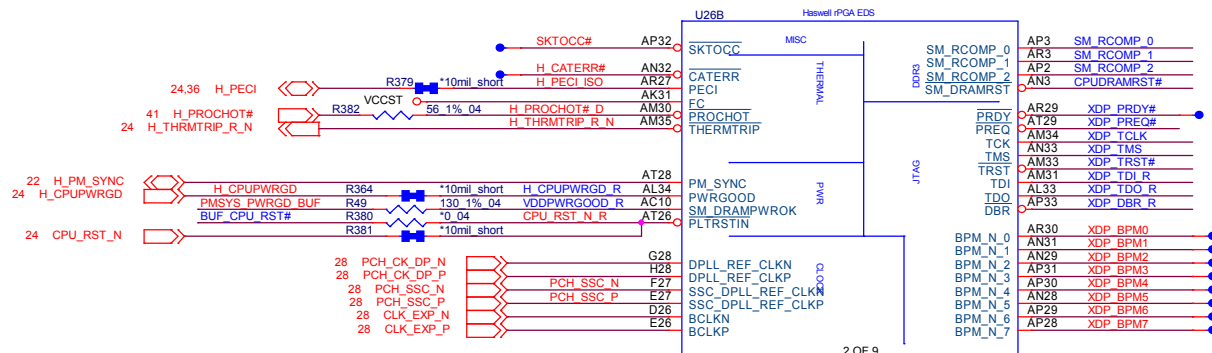
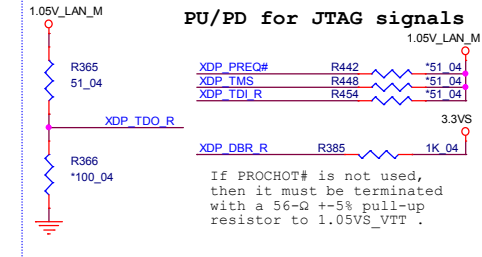
Haswell Processor 2/7 (CLK,MISC,JTAG)



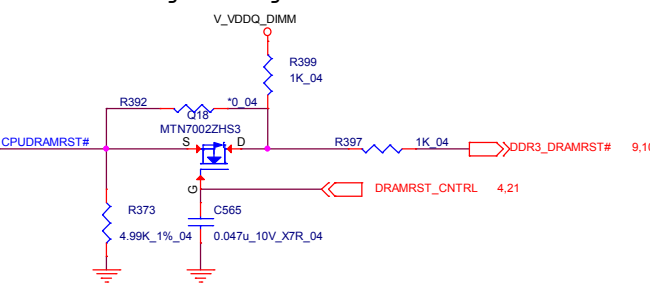
DDR3 Compensation Signals



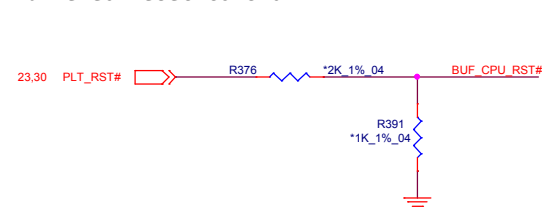
PU/PD for JTAG signals



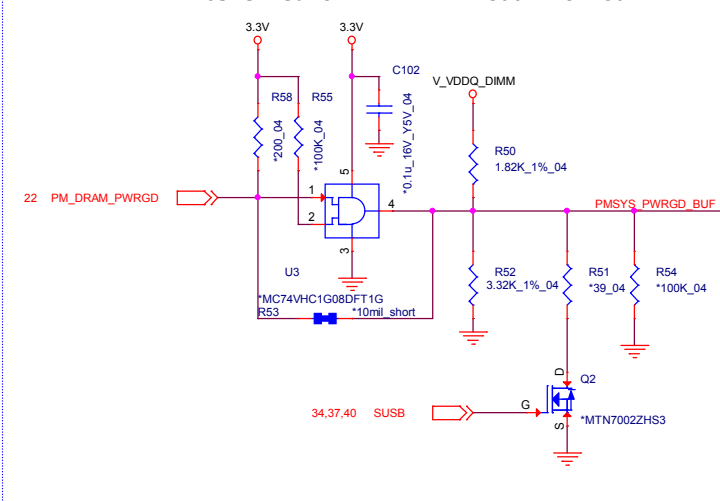
S3 circuit:- DRAM $\overline{\text{RST}}\#$ to memory
should be high during S3



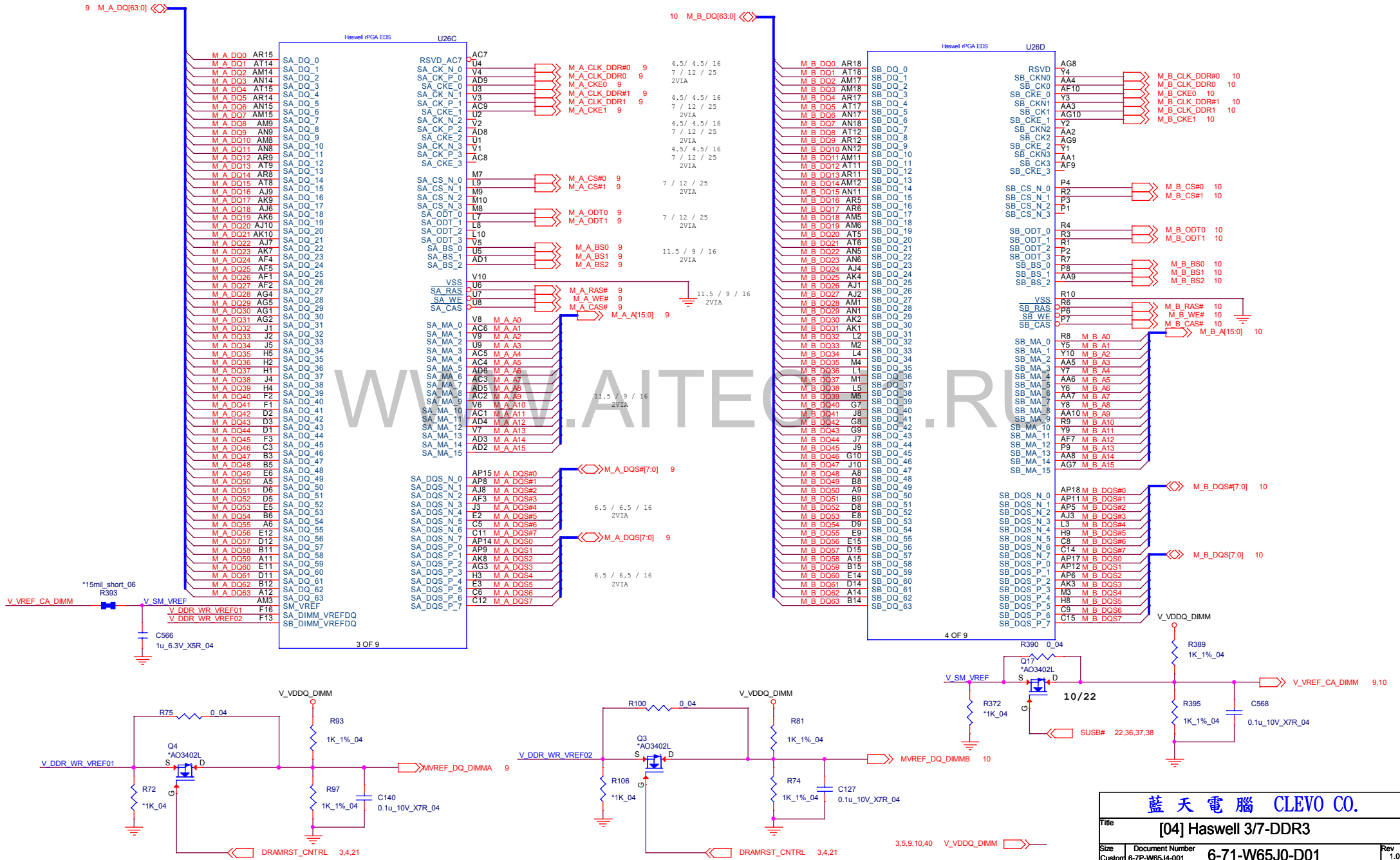
Buffered reset to CPU



S3 circuit:- DRAM PWR GOOD 1CIRCUIT



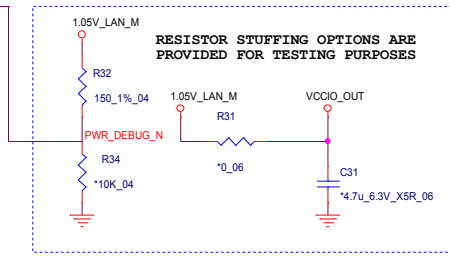
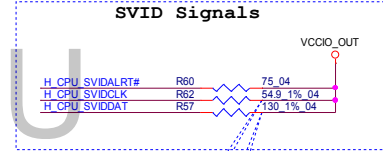
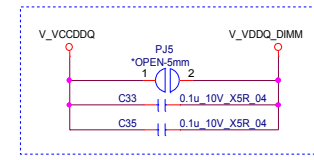
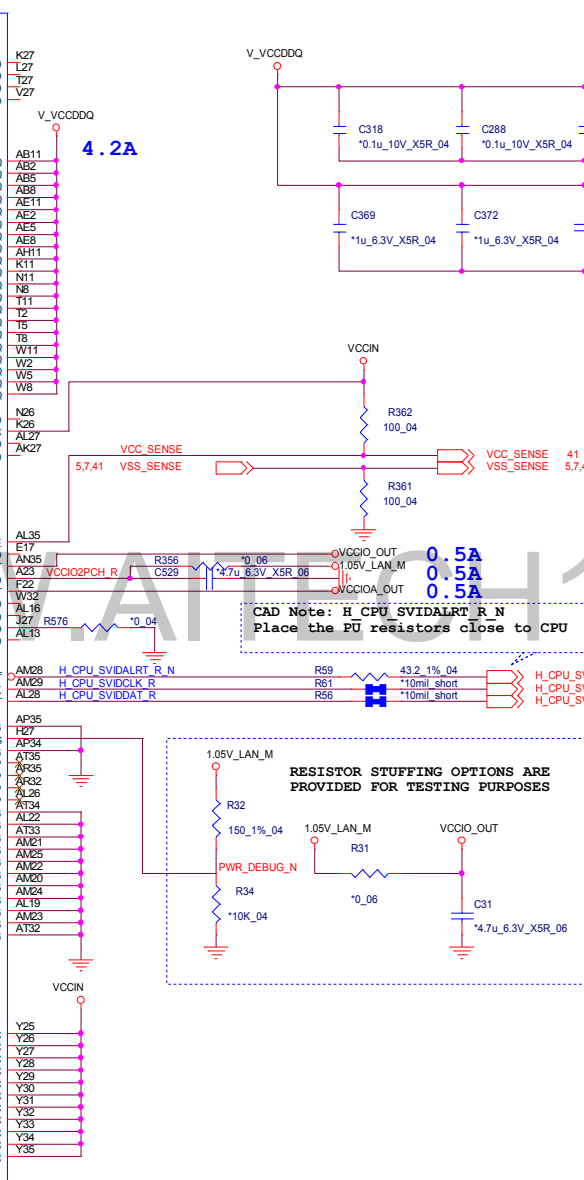
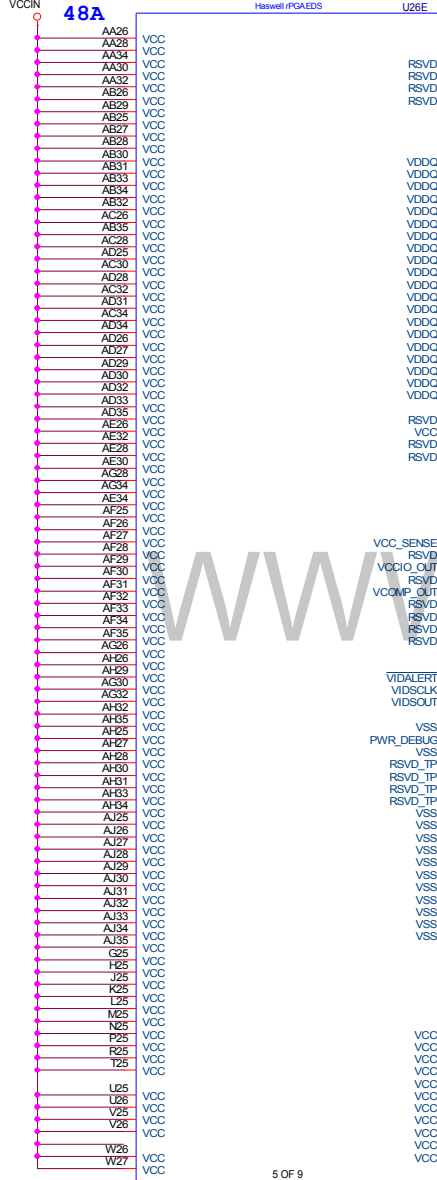
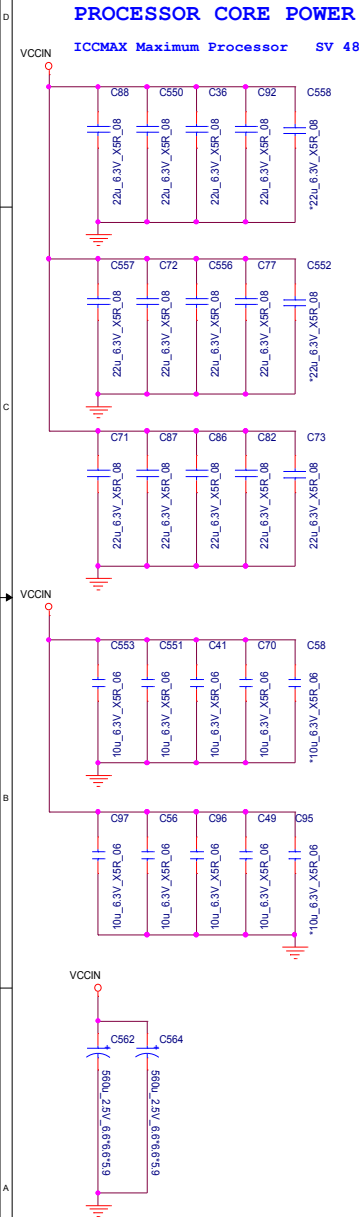
Haswell Processor 3/7 (DDR3)



Haswell Processor 4/7 (POWER)

PROCESSOR CORE POWER

ICCMAX Maximum Processor SV 48



CAD Note: H_CPU_SVIDALRT#
PLACE THE PU RESISTORS
CLOSE TO VR

CAD Note: H_CPU_SVIDCLK
PLACE THE PU RESISTORS
CLOSE TO VR

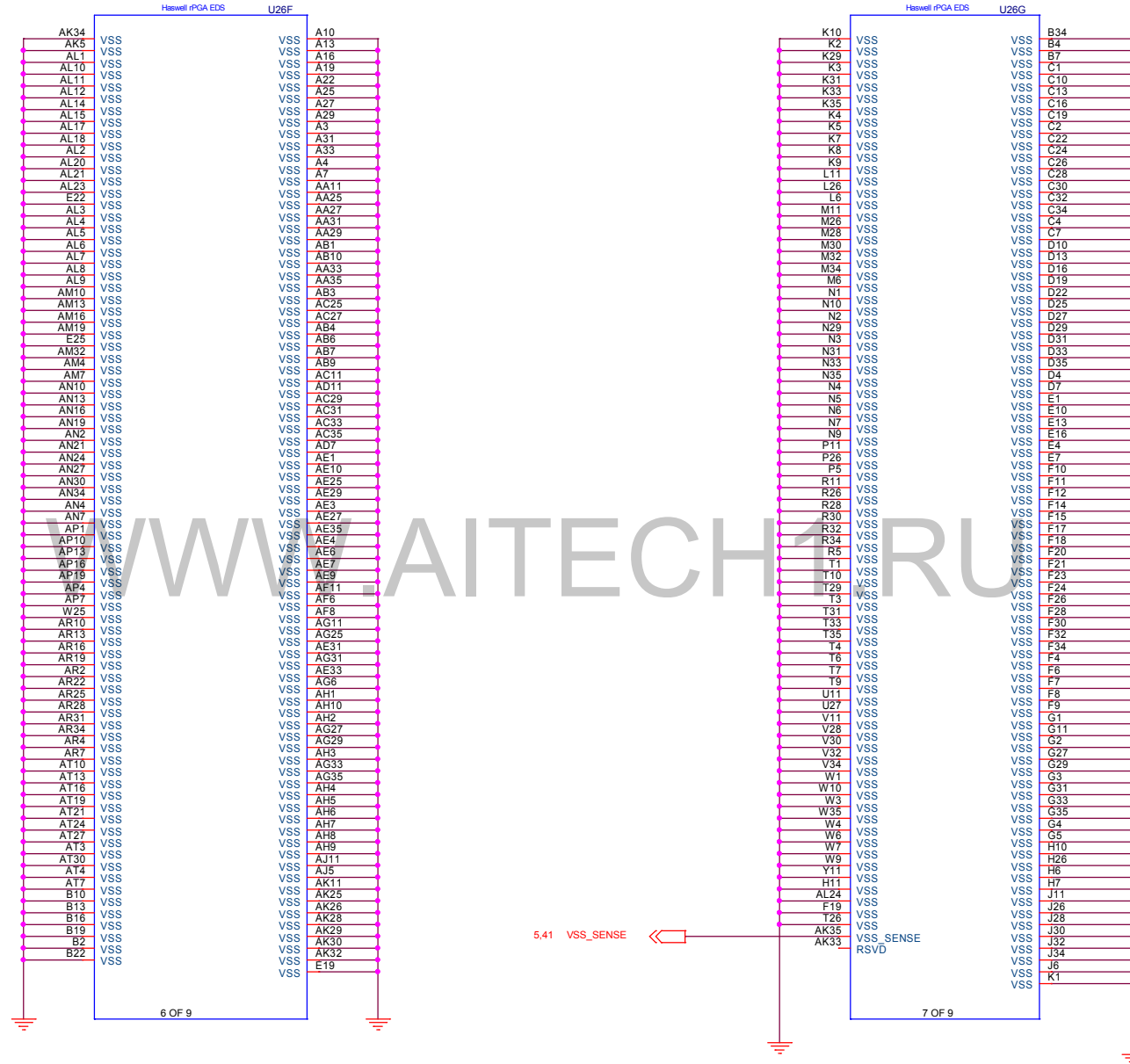
CAD Note: H_CPU_SVIDCLK_R
PLACE THE PU
resistors close to VR

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<div> <div>藍天電腦 CLEVO CO.</div> <div> <div>Title</div> <div>[06]Haswell 5/7-EDP/HDMI</div> </div> </div>			
Size A3	Document Number 6-7P-W65J4-001	6-71-W65J0-D01	Rev 1.0
Date:	Wednesday, October 09, 2013	Sheet 6 of	46

Haswell Processor 6/7 (GND)



Haswell Processor 7/7 (RESERVED)

CFG Straps for Processor

PCI EXPRESS STATIC LANE REVERSAL FOR ALL PEG PORTS

CFG2 1: (DEFAULT) NORMAL OPERATION; (Default)
LANE# DEFINITION MATCHES
0: LANE REVERSAL

CFG2 R66 *1K_04

DISPLAY PORT PRESENCE STRAP

CFG4 1: DISABLED;
NO PHYSICAL DISPLAY PORT ATTACHED
TO EMBEDDED DISPLAY PORT
0: ENABLED; AN EXTERNAL DISPLAY PORT DEVICE
AN EXTERNAL DISPLAY PORT DEVICE IS CONNECTED
TO THE EMBEDDED DISPLAY PORT (Default)

CFG4 R68 *1K_04

PCIe Port Bifurcation Straps

CFG[6:5] 11: DEVICE1 FUNTION 1, DEVICE 1 FUNCTION2 DISABLED
10: DEVICE1 FUNCTION1 ENABLED DEVICE1 FUNCTION 2 DISABLED
01: DEVICE 1 FUNCTION 1 DISABLED, DEVICE 1 FUNCTION 2 ENABLED
00: DEVICE 1 FUNCTION 1 ENABLED, DEVICE 1 FUNCTION 2 ENABLED

CFG5 R64 *1K_04

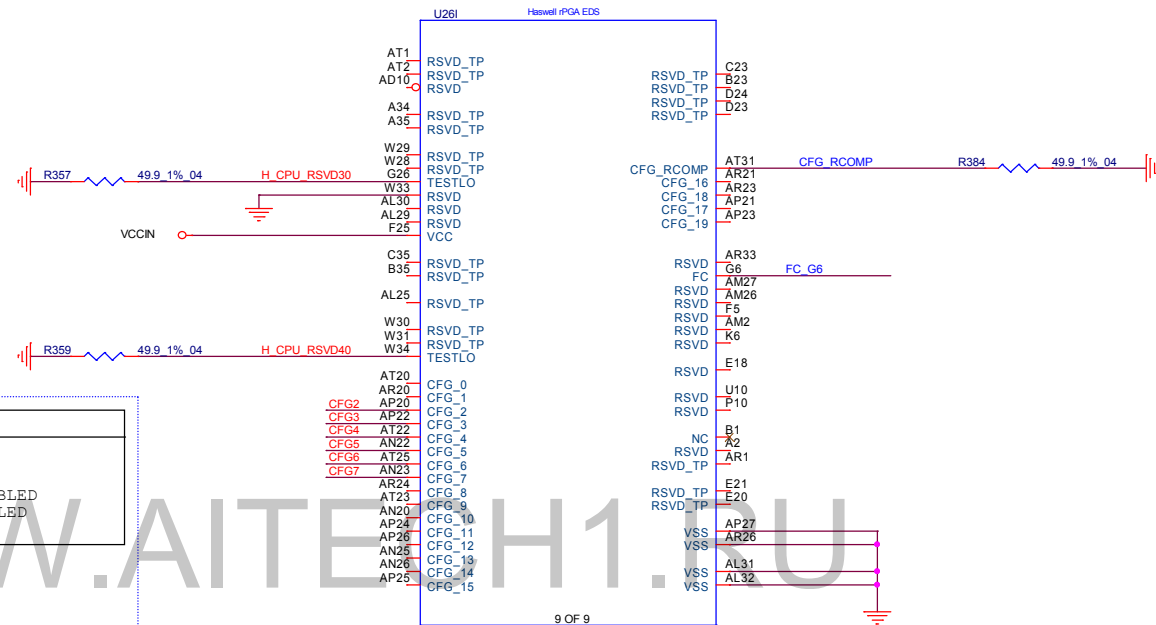
CFG6 R378 *1K_04

DEFENSIVE PULL DOWN SITE

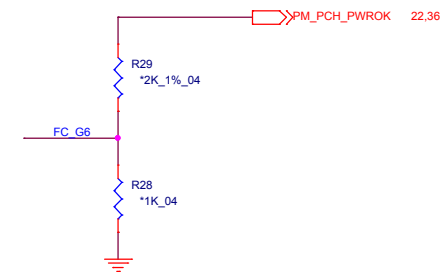
CFG7 R63 *1K_04

NOTE:
PHYSICAL DEBUG ENABLED (DFX PRIVACY)
0 : ENABLED SET DFX ENABLED BIT IN DEBUG INTERFACE MSR
1 : DISABLED

CFG3 R67 *1K_04



NOTE:
RESERVE THIS CIRCUIT FOR FUTURE COMPATIBILITY



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Title [08] PROCESSOR 7/7

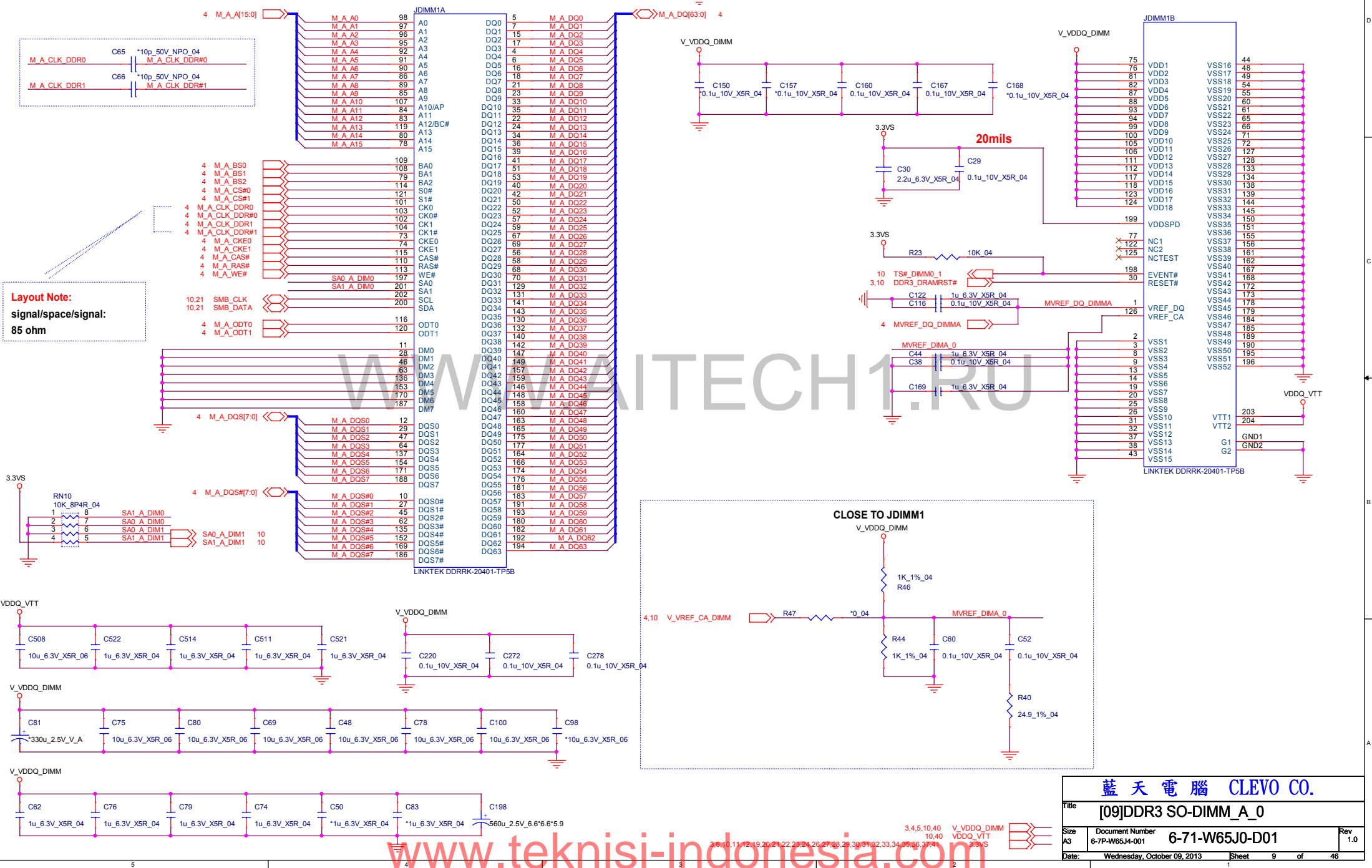
Size Custom Document Number 6-7P-W65J4-001 6-71-W65J0-D01 Rev 1.0

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SO-DIMM A_0

CHANGE TO STANDARD

5.2mm
6-86-24204-005
6-86-24204-002



藍天電腦 CLEVO CO.

[09]DDR3 SO-DIMM_A_0

Document Number 6-71-W65J0-D01

Date: Wednesday, October 09, 2013

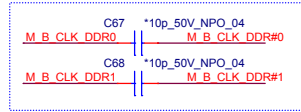
Rev 1.0

Sheet 9 of 46

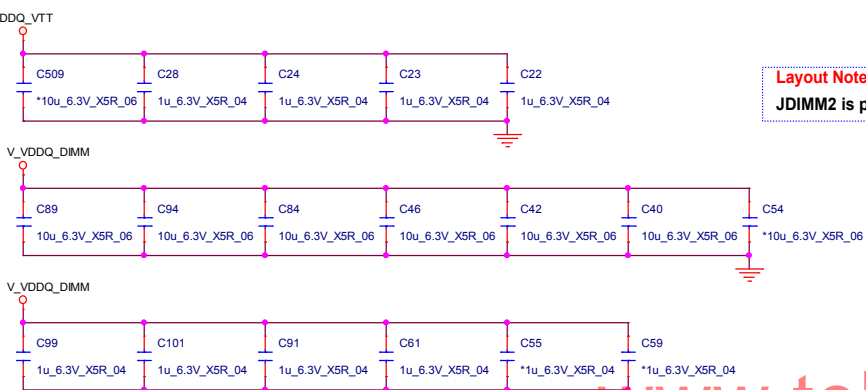
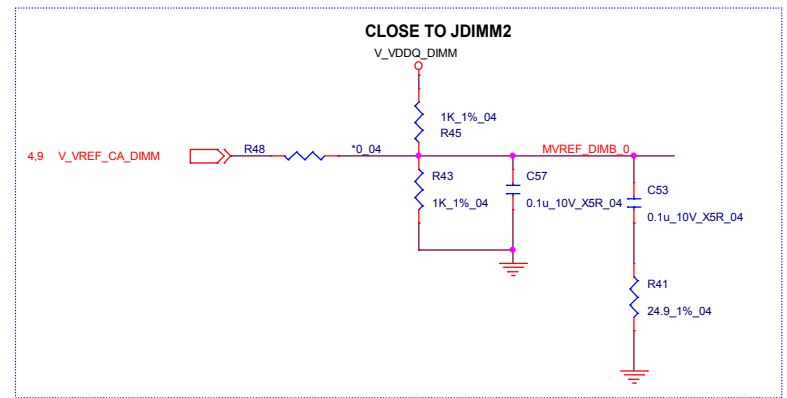
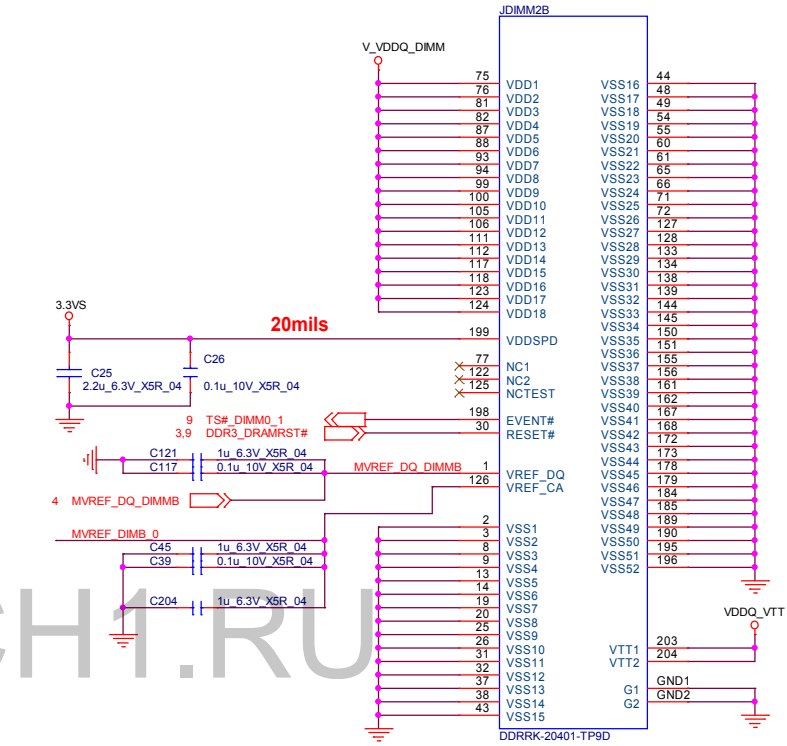
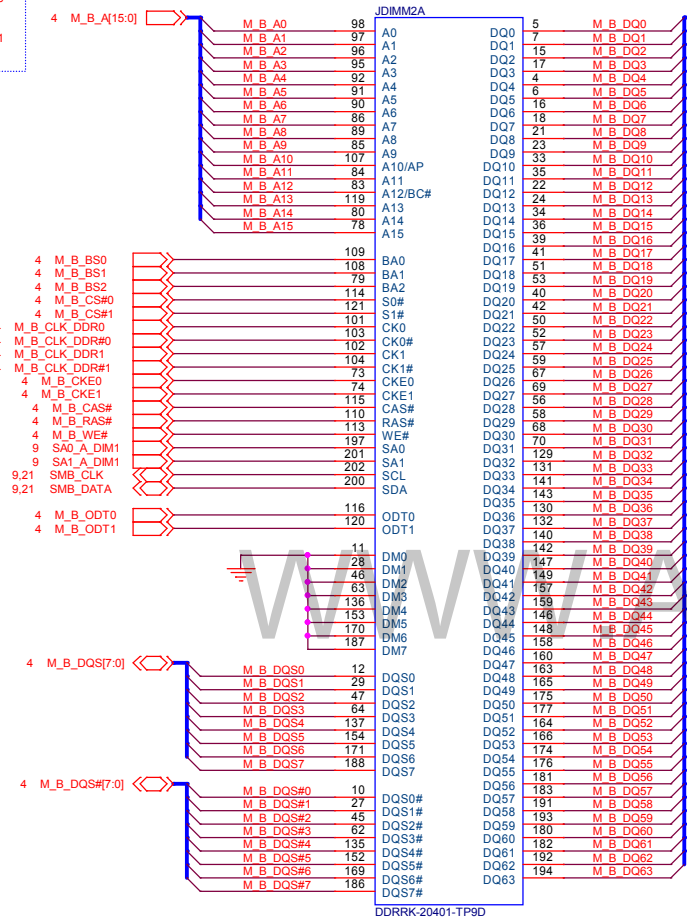
SO-DIMM B_0

CHANGE TO STANDARD

9.2mm
6-86-24204-001
6-86-24204-006

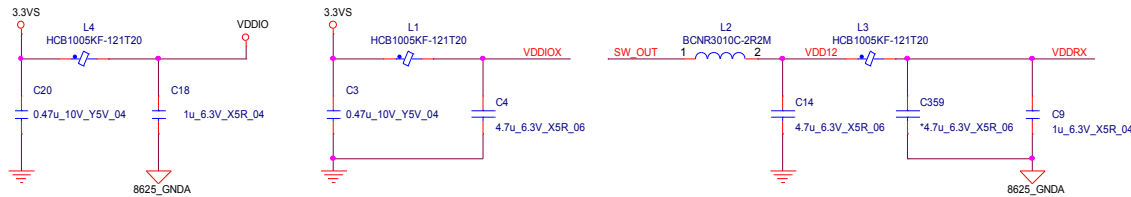


Layout Note:
signal/space/signal:
8 / 4 / 8

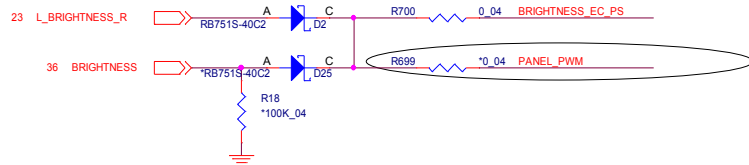
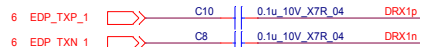
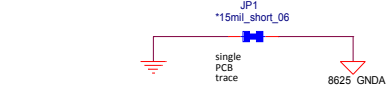


Layout Note:
JDIMM2 is placed farther from the GMCH than JDIMM1

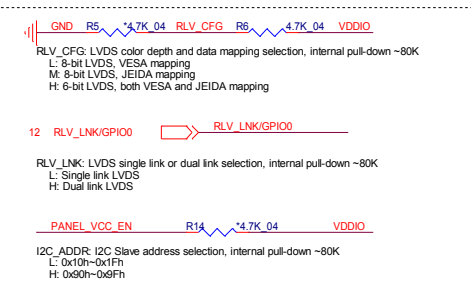
藍天電腦 CLEVO CO.			
[10]DDR3 SO-DIMM_B_0			
Size A3	Document Number 6-71-W65J0-D01	Rev 1.0	
Date: Wednesday, October 09, 2013	Sheet 10	of 46	



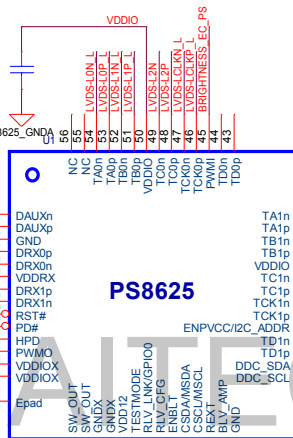
- Place the switching regulator inductor (L3) close to SW_OUT Pins (Pin15, Pin16).
- The SW_OUT output traces should be as wide as possible.
- The GND pins (Pin17, Pin18) should be connected to the main PCB ground plane, with the device GND pins of the PS8625 connected to separate GND island (GND) for the device. The GND island (GND) should be connected to the main GND plane (GND) with a single-point connection by use of a wide PCB trace.
- Place the 4.7uF decoupling Capacitor (C4) for VDDIOX close to VDDIO pin.
- The GND of the 4.7uF capacitor (C4) for VDDIOX should be placed close to the GND of 4.7uF capacitor (C5) behind inductor.
- Place the bead (L2) for VDDIOX close to PS8625.



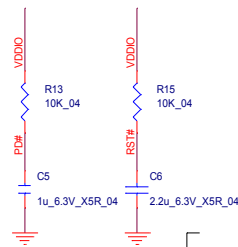
Power On Configuration



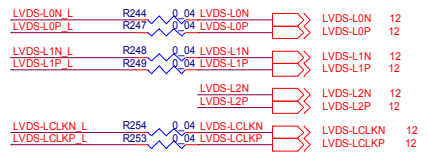
36 SMC_EDP_CLK
36 SMD_EDO_DAT



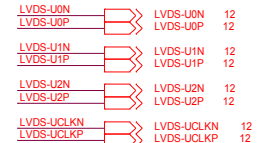
6-03-08625-030



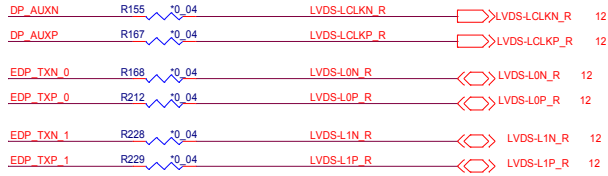
Single link
LVDS



Dual link
LVDS



To LVDS Connector



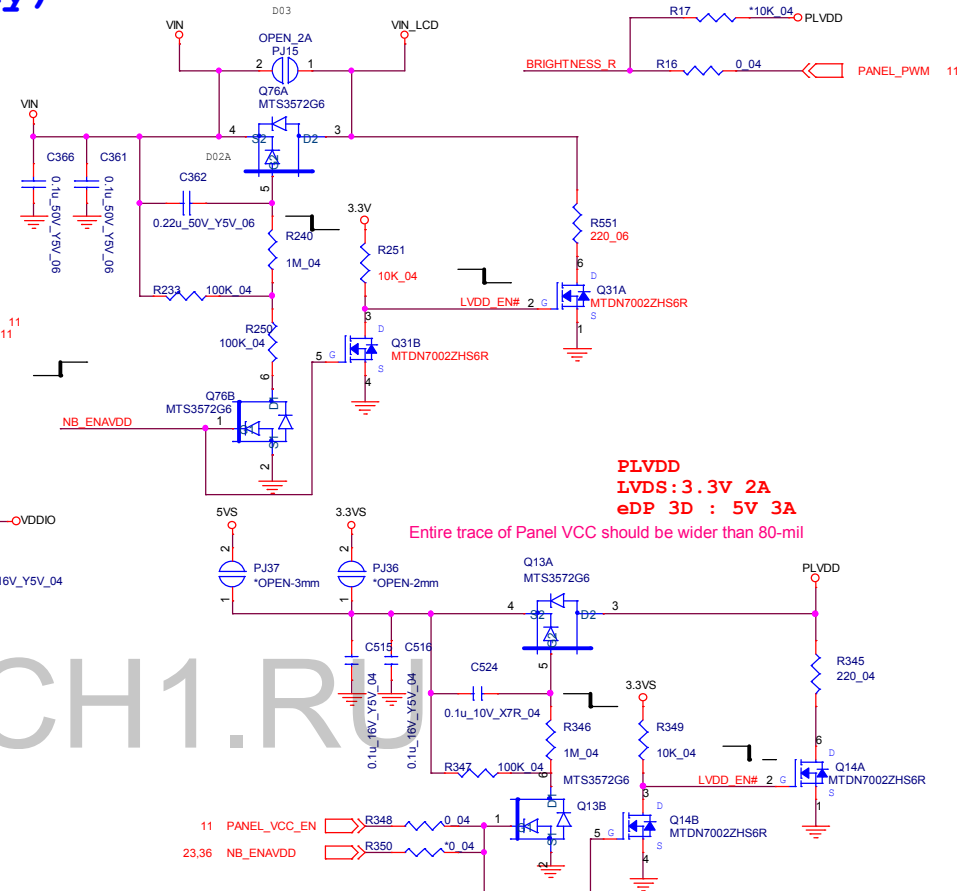
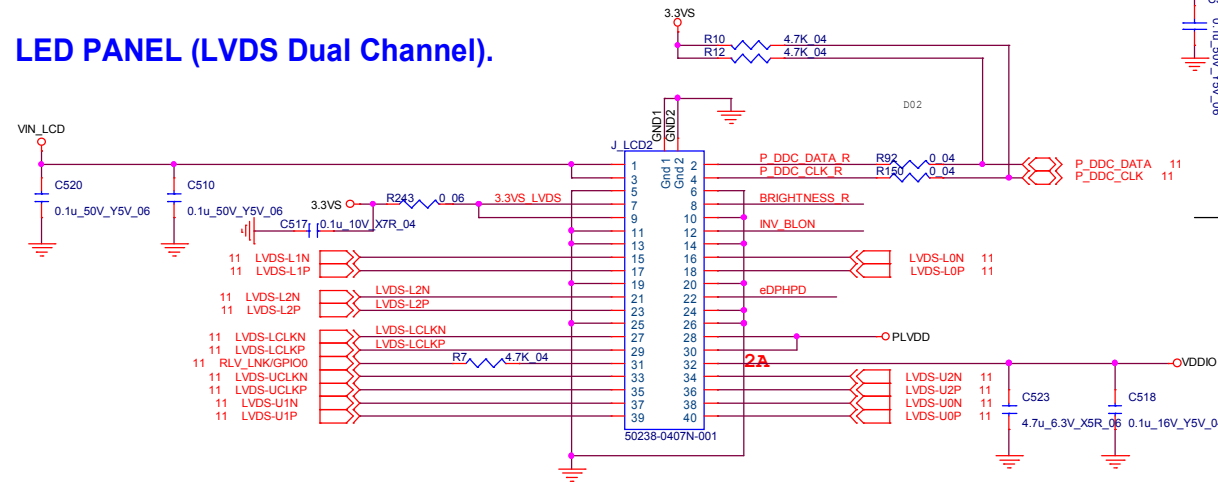
藍天電腦 CLEVO CO.			
Title [11] PS8625(EDP TO LVDS)			
Size Custom	Document Number 6-7P-W65J4-001	6-71-W65J0-D01	
Date: Wednesday, October 09, 2013	Sheet 11	of 46	

PANEL CONNECTOR (30Pin_eDP & 40Pin_LVDS CO-lay)

PANEL POWER

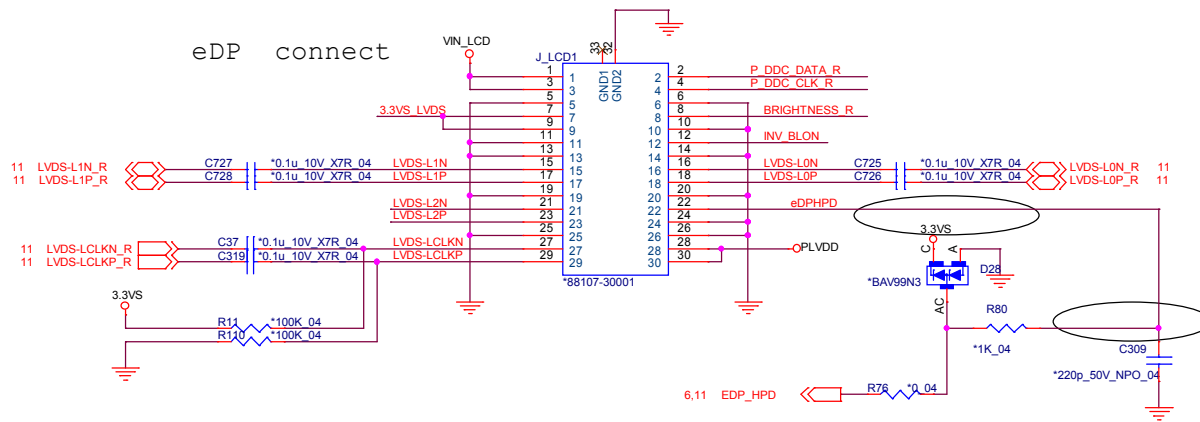
BRIGHTNESS

LED PANEL (LVDS Dual Channel).

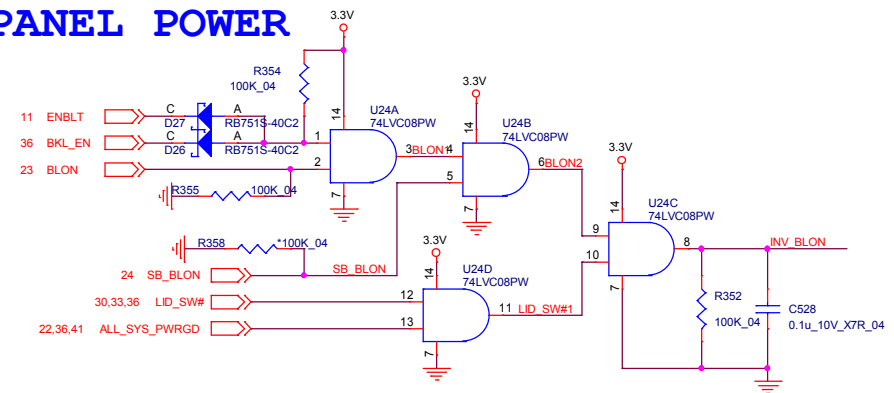


WWW.AITECH1.RU

eDP connect



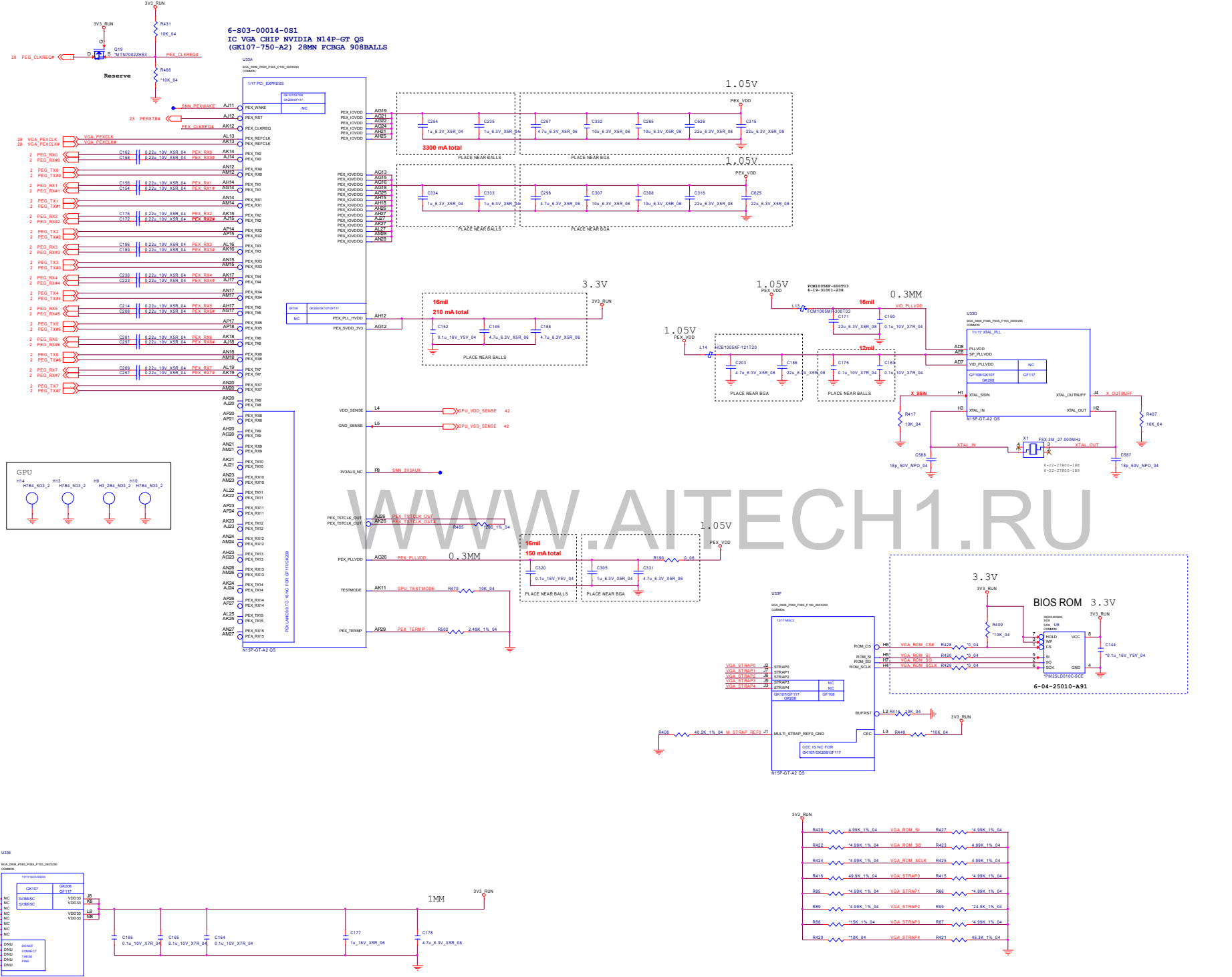
PANEL POWER



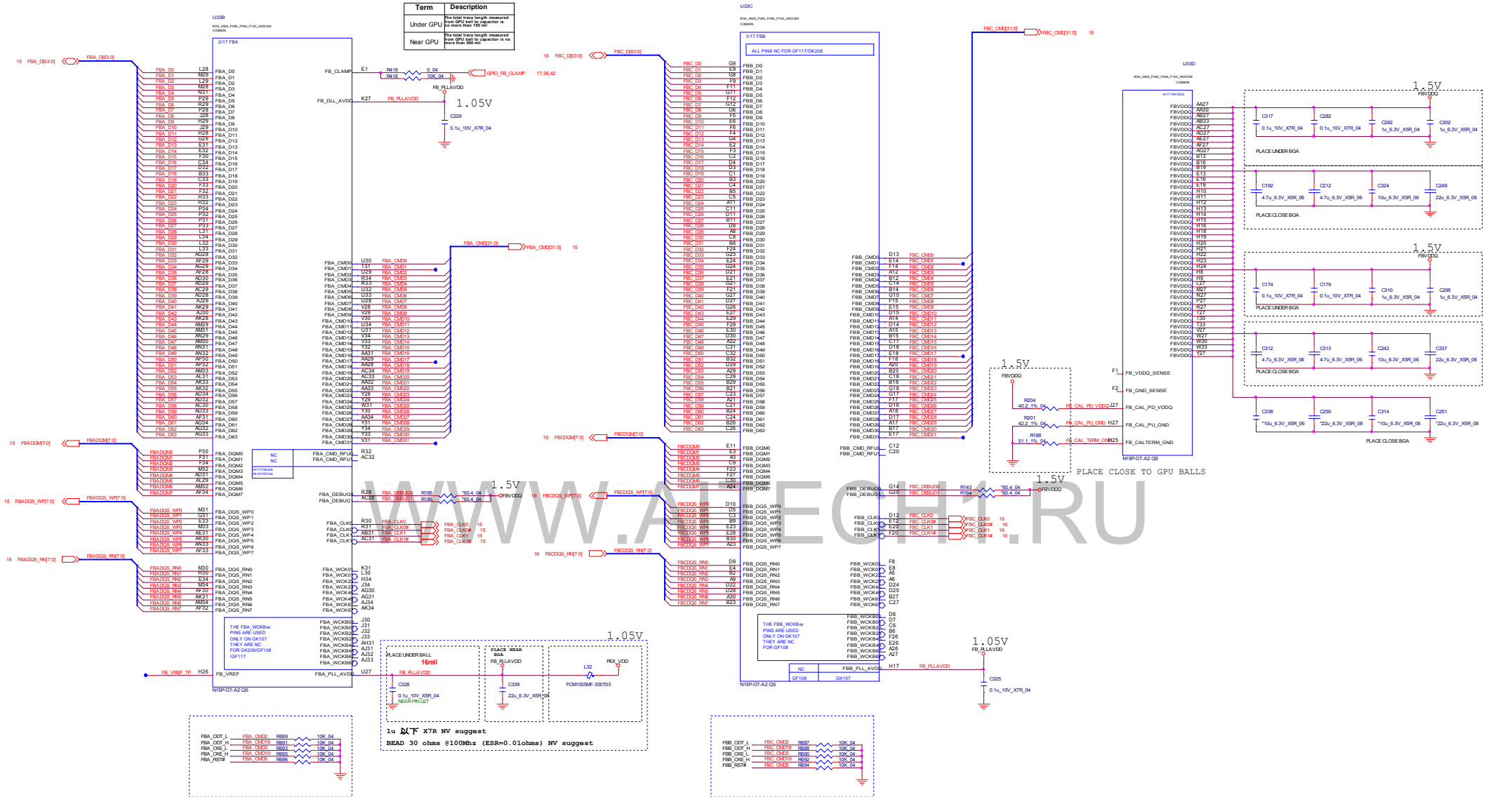
藍天電腦 CLEVO CO.

Title			
[12]PANEL,INVERTER			
Size	Document Number	Rev	
6-7P-W65J4-001	6-71-W65J0-D01	1.0	
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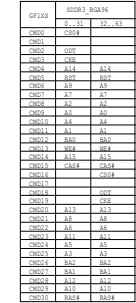
6-803-00014-081
IC VGA CHIP NVIDIA N14P-GT QS
(GK107-750-A2) 28MN FCBGA 908BALLS



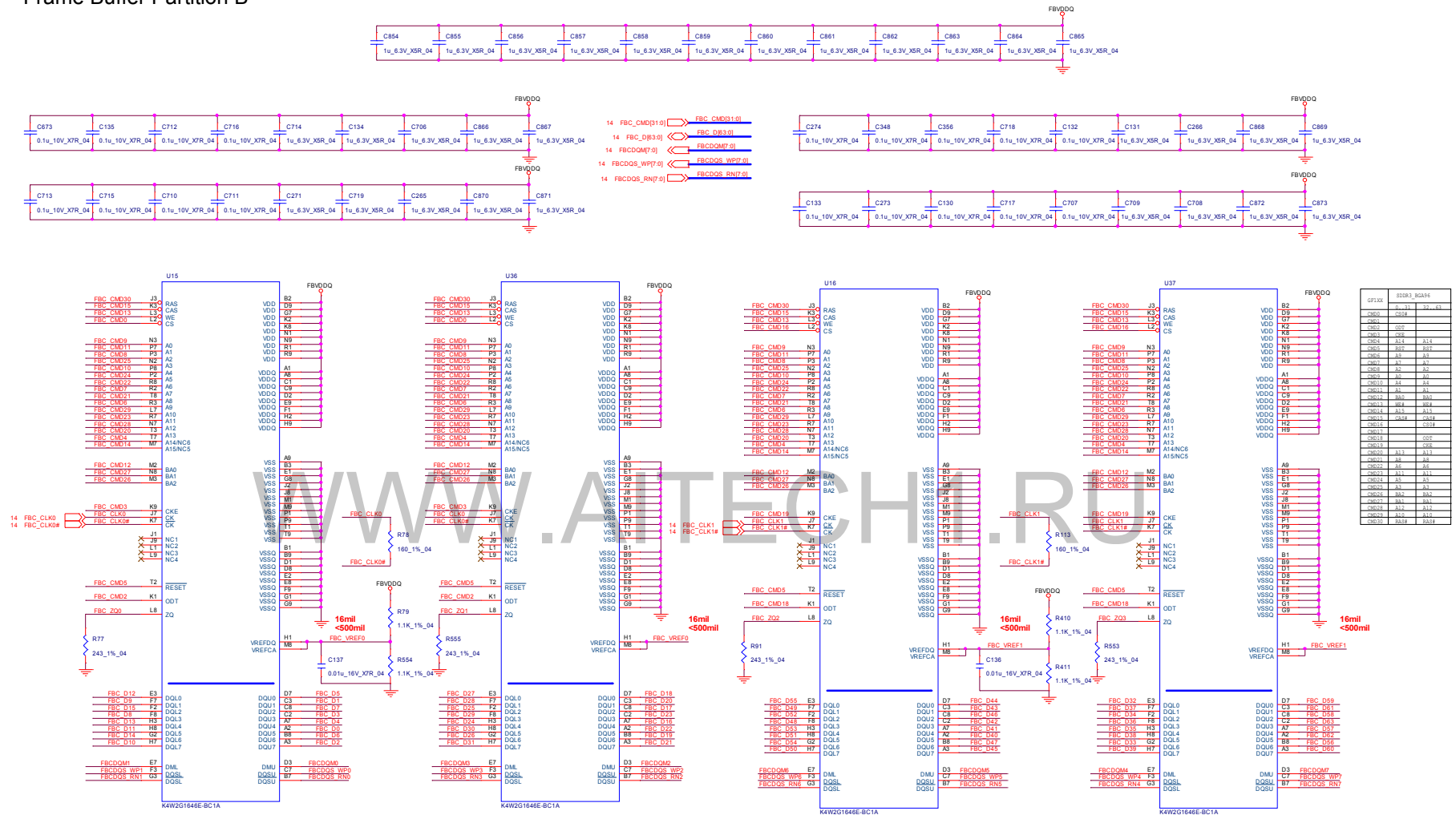
Term	Description
Under GPU	The total trace length measured from GPU ball to capacitor is no more than 150 mil
Near GPU	The total trace length measured from GPU ball to capacitor is no more than 850 mil



0.1uF X4 for FBVDDQ one chip VRAM
1.0uF X8

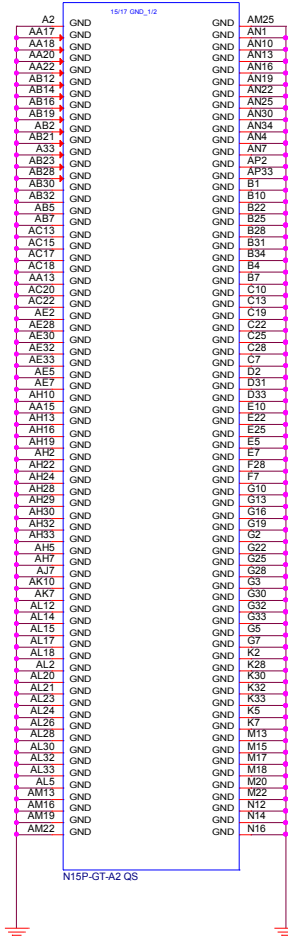


Frame Buffer Partition B



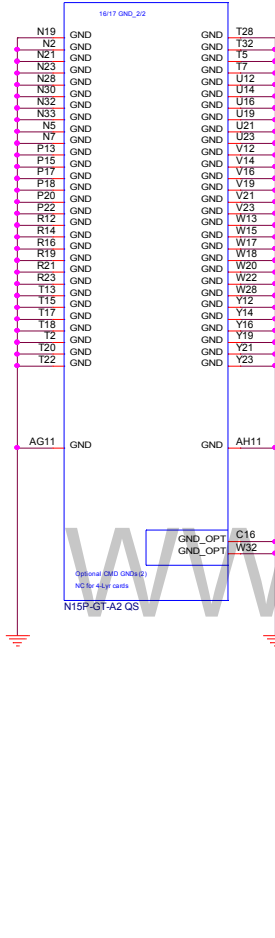
U33F

BGA_0908_P080_P085_P100_200x290
COMMON



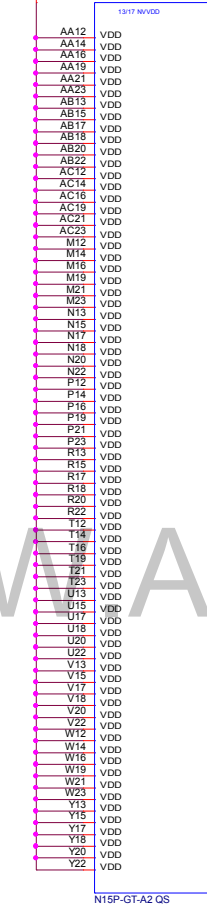
U33G

BGA_0908_P080_P085_P100_200x290
COMMON



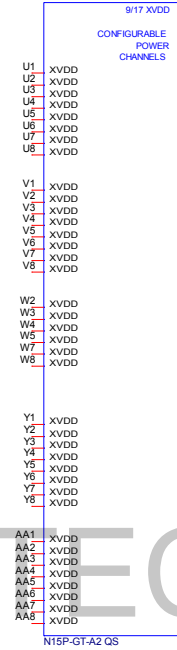
U33H

BGA_0908_P080_P085_P100_200x290
COMMON

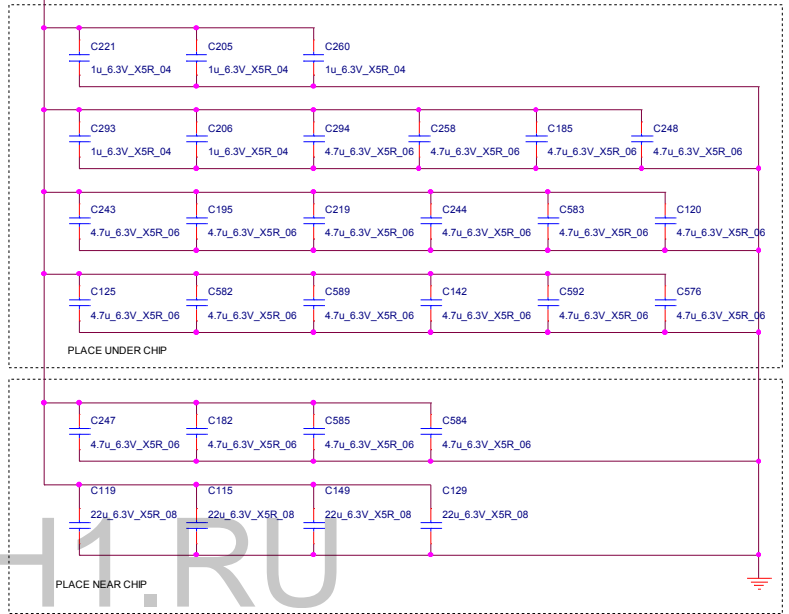


U33I

BGA_0908_P080_P085_P100_200x290
COMMON



NVDD



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Title [18] VGA NVDD Decoupling

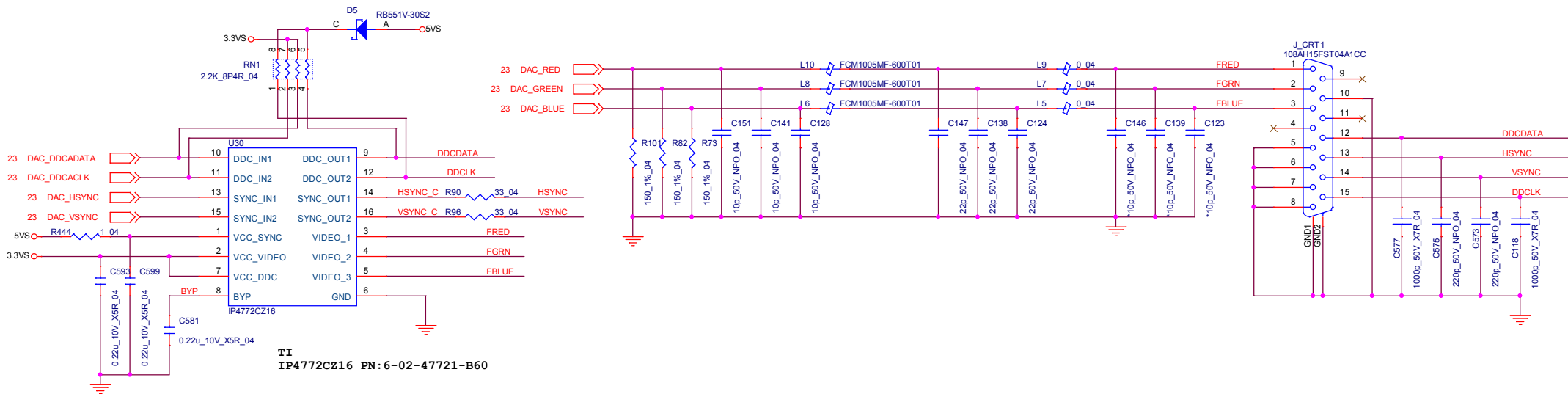
Size	Document Number	6-71-W6500-D03	Rev	3.0
Custom	6-7P-W65J4-001			
Date:	Wednesday, October 09, 2013	Sheet	18	of 46

17,20,21,22,24,26,27,28,29,30,32,33,36,37,38,39,43

42 VDD3
13,17,42 NVDD
3V3_RUN

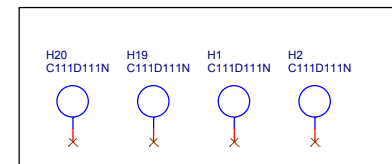
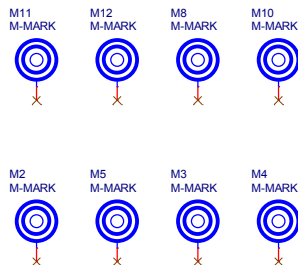
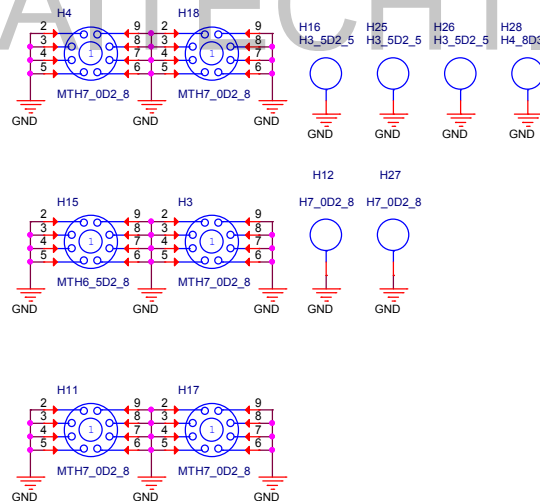


CRT



WWW.ALTECH1.RU

M/B PCH



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[19]CRT

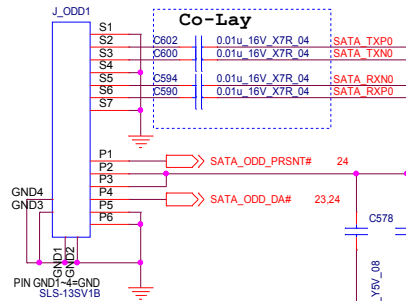
Size A3	Document Number 6-7P-W65J4-001	Rev 1.0
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Lynx Point - M (HDA, JTAG, SATA)

ODD CONN

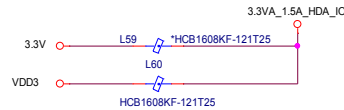
W650SR



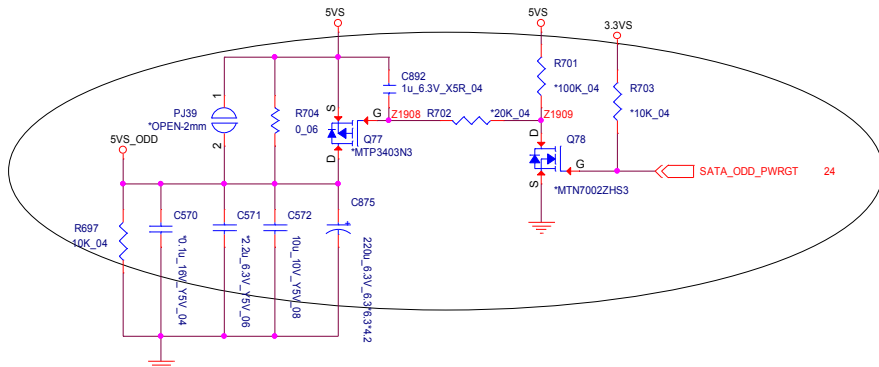
JOPEN1	CLEAR COMS
JOPEN2	CLEAR ME RTC REGISTER

Flash Descriptor
Security Override
Low = Disabled (Default)
High = Enabled

INTVPMEN - INTEGRATED SUS 1.05V VRM ENABLE
HIGH - ENABLE INTERNAL VR'S
LOW - ENABLE EXTERNAL VR'S

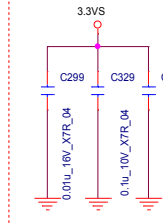


ODD Zero Power ODD



NO REBOOT STRAP
NO REBOOT STRAP: HDA_SPKR High Enable

Layout Note:



27	3.3VA_1.5A_HDA_IO	1.5VS
22,27	RTC_VCC	5VS
12,19,30,33,34,35,37,41,42	VDD3	3.3V
2,3,12,17,24,25,28,29,31,33,35,37,38,40,42	VDD3	3.3VS
3,6,9,10,11,12,19,21,22,23,24,26,27,28,29,30,31,32,33,34,35,36,37,41	VDD3	3.3VS

SATA ODD

BC8	SATA_RXN0
BE8	SATA_RXP0
AW8	SATA_TXN0
AY8	SATA_TXP0

E-SATA HDD

BB9	SATARXN2	31
BD9	SATARXP2	31
AY13	SATATXN2	31
AW13	SATATXP2	31

MSATA HDD

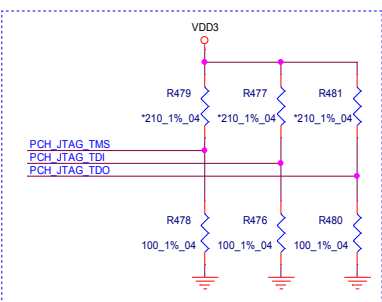
BD13	SATA_RXN4	29
BB13	SATA_RXP4	29
AV15	SATA_TXN4	29
AW15	SATA_TXP4	29

SATA HDD1

BC14	SATA_RXN5	33
BE14	SATA_RXP5	33
AP15	SATA_TXN5	33
AR15	SATA_TXP5	33

Boot BIOS Strap

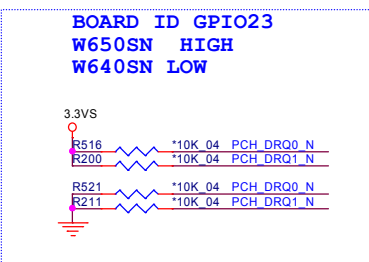
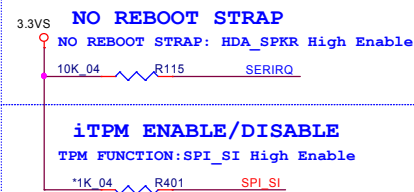
BBS_BIT1	BBS_BIT0	Boot BIOS Location
0	0	LPC (Default)
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI



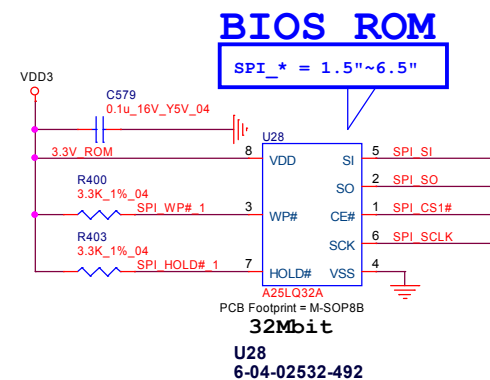
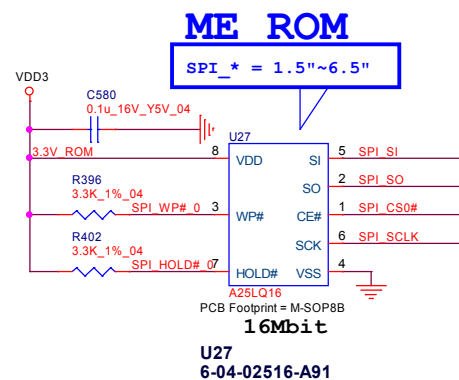
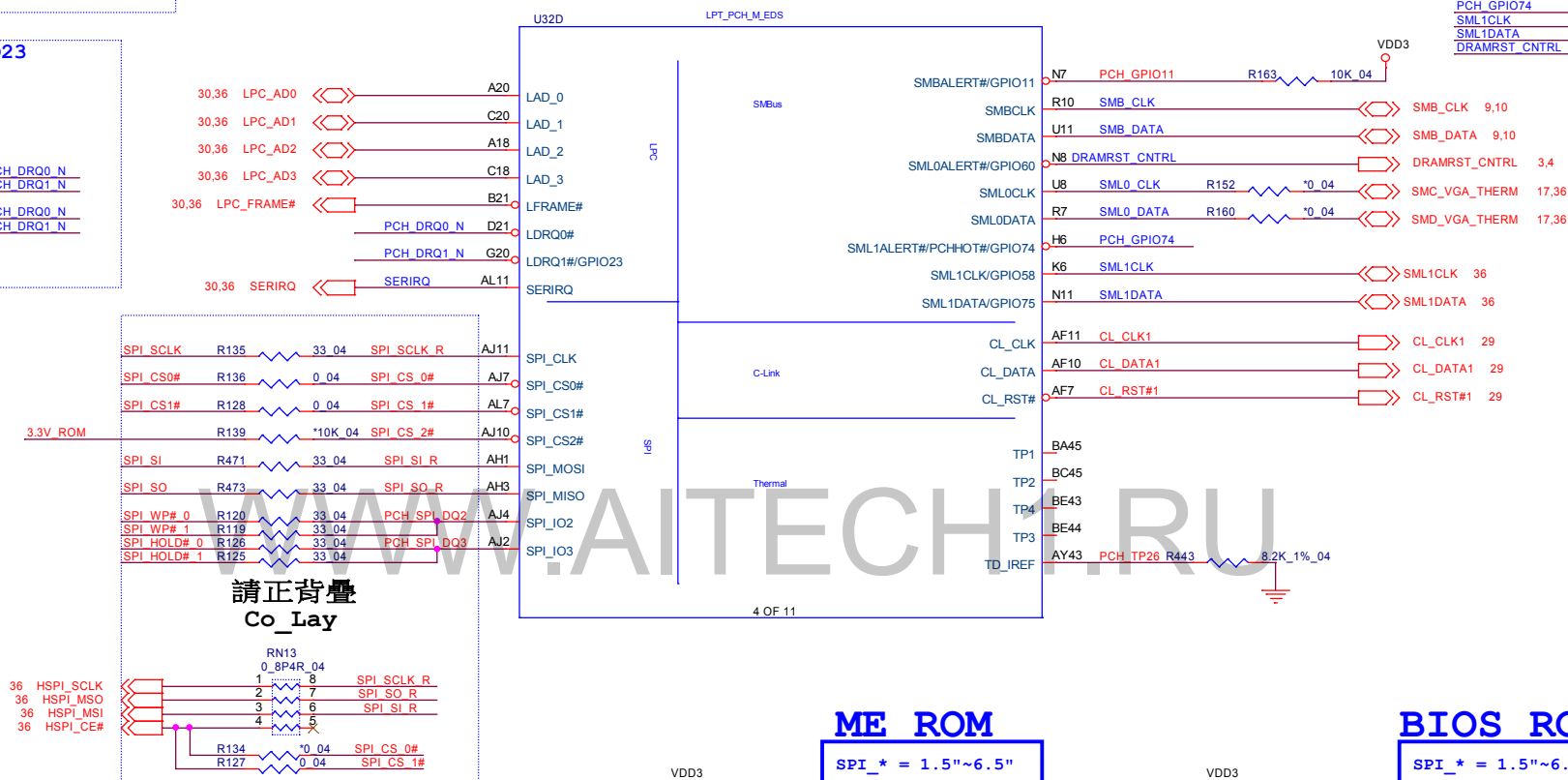
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[20] Lynx 1/9-RTC/HDA/SATA

Size	Document Number	Rev
Custom	6-71-W65J0-D01	1.0
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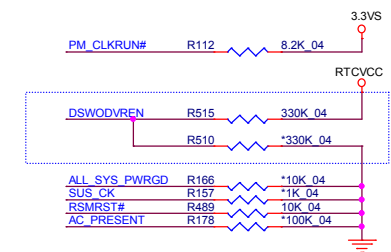
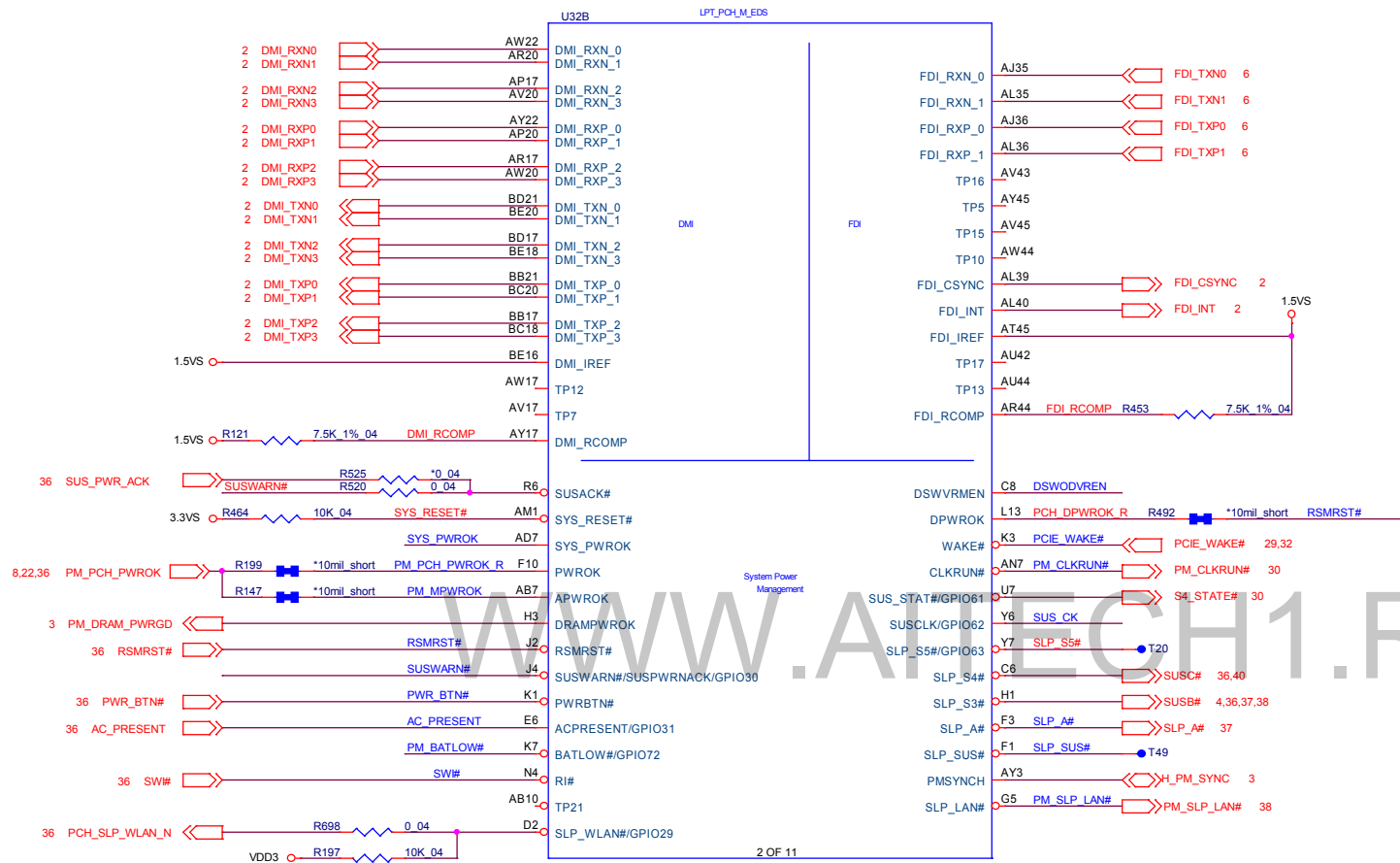


Lynx Point - M (LPC, SMBUS, SPI, C-LINK)

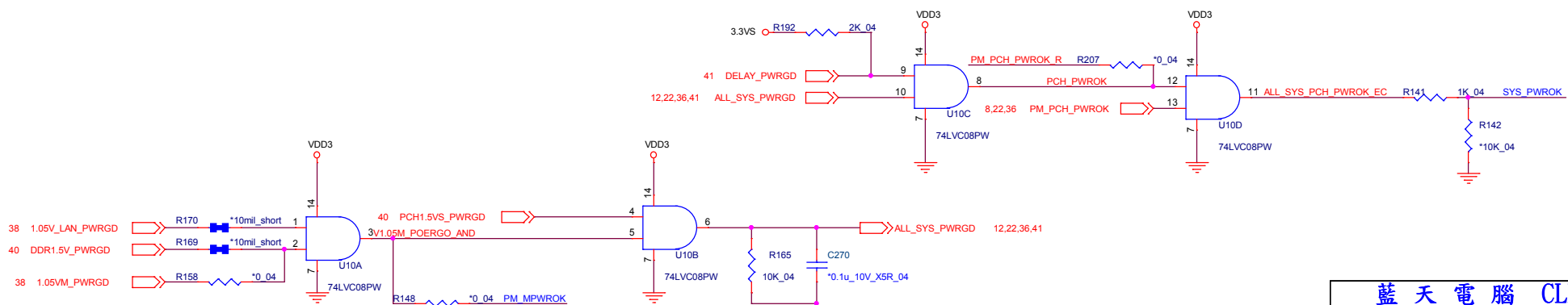
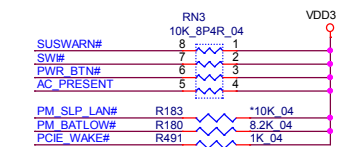


藍天電腦 CLEVO CO.			
Title [21] Lynx 2/9-LPC/SPI/LPC/SMB			
Size Custom	Document Number 6-7P-W65J4-001	6-71-W65J0-D01	
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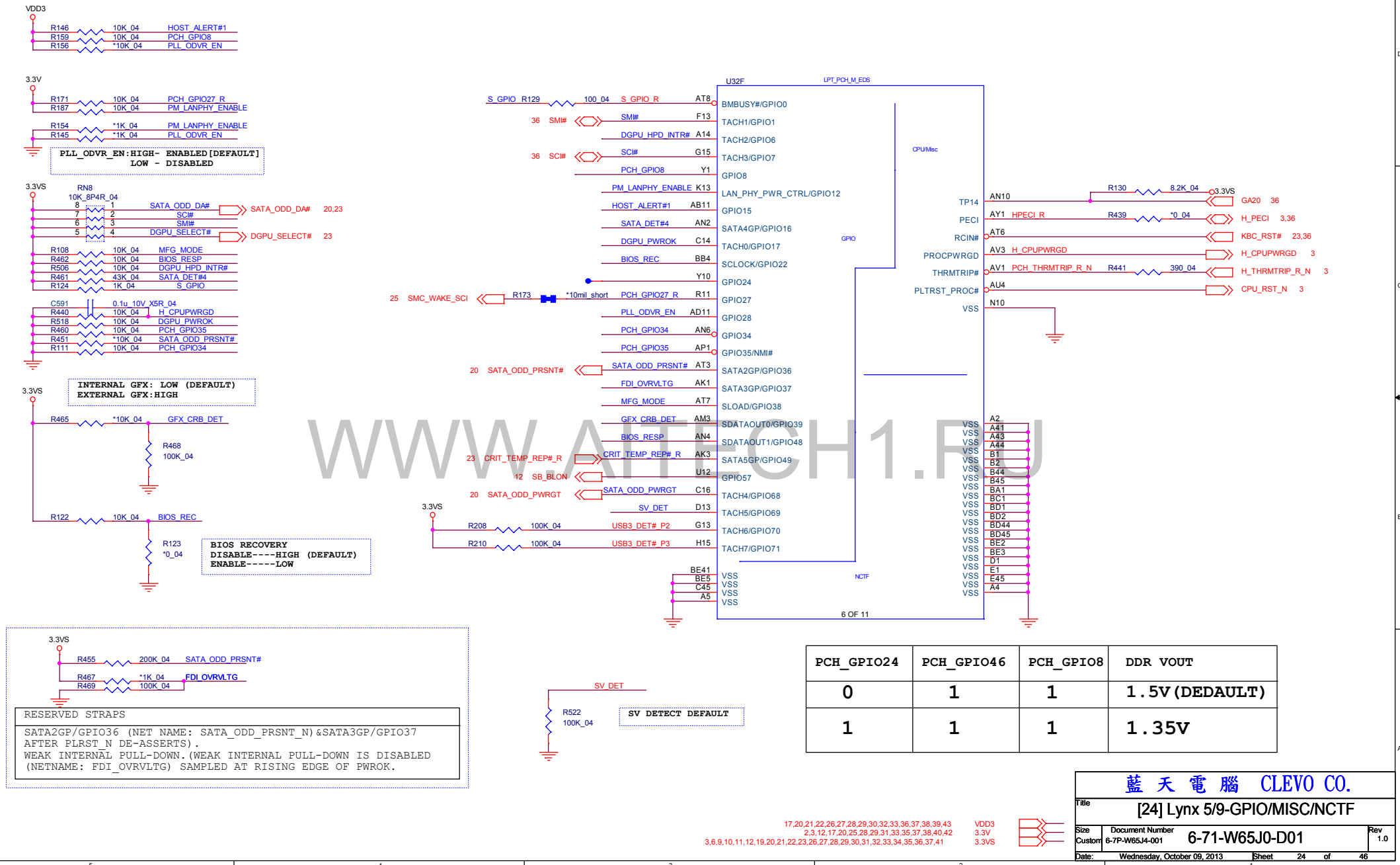
Lynx Point -M (DMI,FDI,PWRGD)



DSWODVREN - ON DIE DSW VR ENABLE	
R426	Enabled (DEFAULT)
R427	Disabled

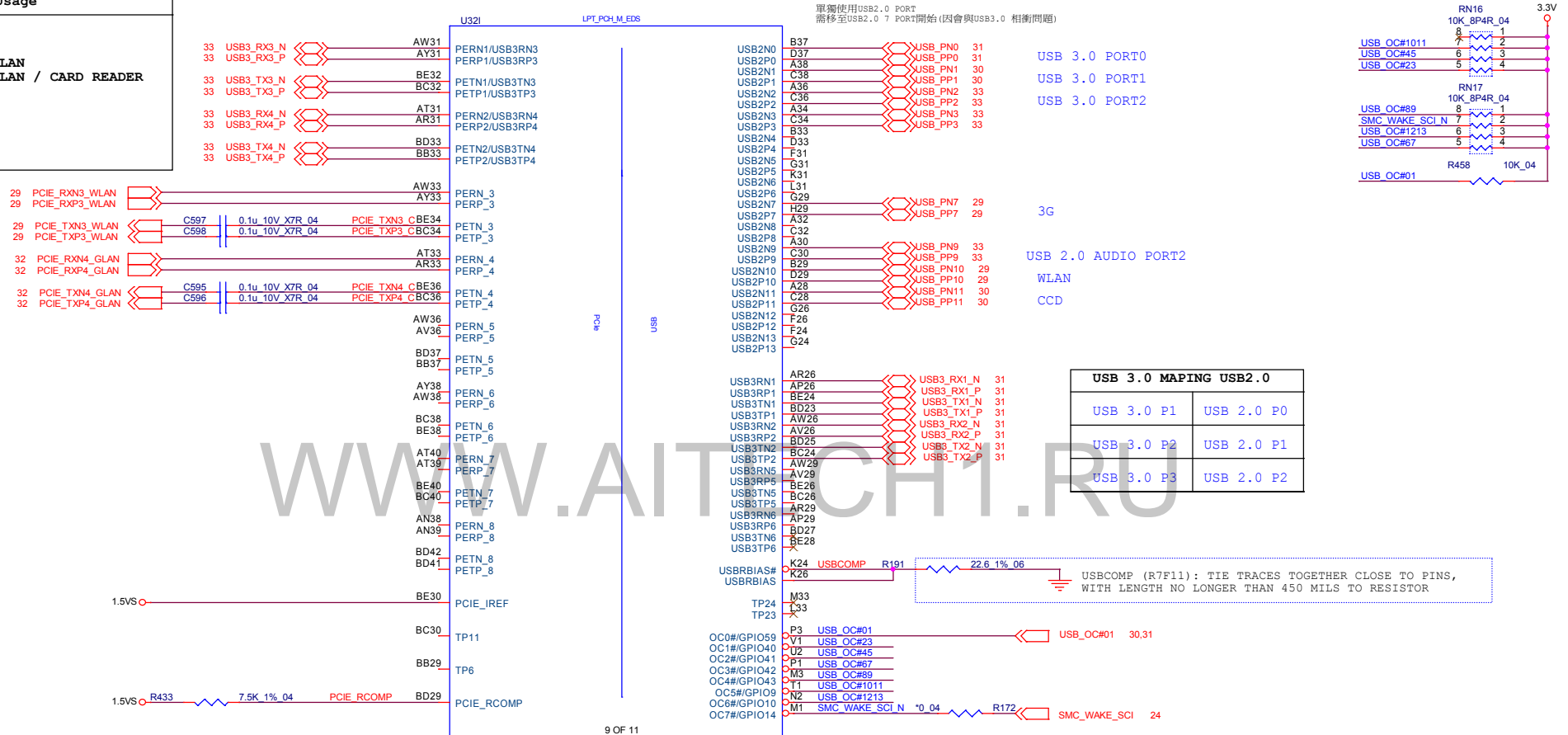


Lynx Point - M (GPIO,CPU/MISC,NCTF)



Lynx Point -M (PCIe,USB)

PCI-E x1	Usage
Lane 1	X
Lane 2	X
Lane 3	WLAN
Lane 4	GLAN / CARD READER
Lane 5	X
Lane 6	X
Lane 7	X
Lane 8	X

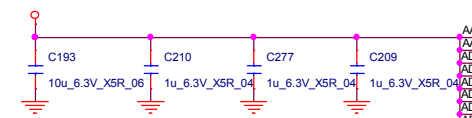


藍天電腦 CLEVO CO.

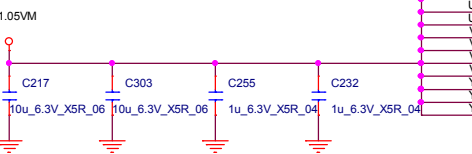
Title			[25] Lynx 6/9-PCIe/USB
Size	Document Number	6-71-W65J0-D01	
Custom	6-7P-W65J4-001	Rev 1.0	
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1.5A

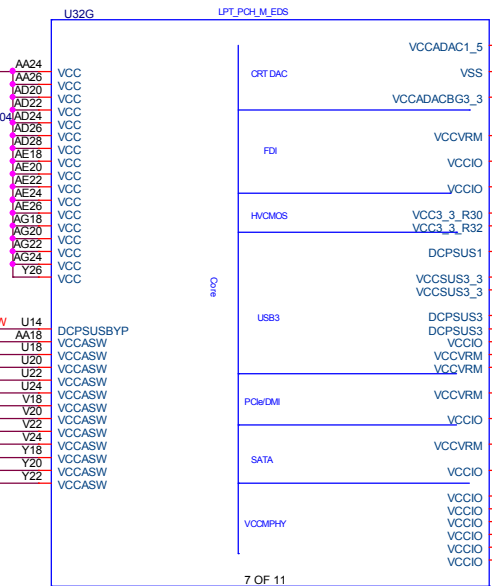
1.05V_LAN_M



1A 5.11_1%_04 change to 5.1_1%_04



Lynx Point -M (POWER)



0.1A

VCCA_DAC_1_2

CO-LAY

HCB1608KF-121T25

VCCA_DAC1.5V

CO-LAY

L24

HCB1608KF-121T25

1.5VS

C287

0.01u_16V_X7S_04

C286

0.1u_10V_X5R_04

C275

10u_6.3V_X5R_06

C304

22u_6.3V_X5R_06

C289

22u_6.3V_X5R_06

*100u_6.3V_B_A

C615

0.1u_10V_X5R_04

C326

1u_6.3V_X5R_04

C327

1u_6.3V_X5R_04

C323

22u_6.3V_X5R_06

C301

22u_6.3V_X5R_06

0.02A

0.3VS

0.2A

1.5VS

C170

*0.1u_10V_X5R_04

C279

0.1u_10V_X5R_04

L11

*HCB1608KF-121T25

V1.05M VCCSUS

0.1A

1.05V_LAN_M

C245

*1u_6.3V_X5R_04

L31

HCB1608KF-121T25

VDD3

C330

0.1u_10V_X5R_04

V3.3A VCCPSUS

0.3A

L19

*HCB1608KF-121T25

VCCA USBUS

0.5A

C216

*10u_6.3V_X5R_06

C211

*1u_6.3V_X5R_04

1.5VS

C891

*10u_6.3V_X5R_06

C187

*10u_6.3V_X5R_06

1.05V_LAN_M

C261

1u_6.3V_X5R_04

C183

1u_6.3V_X5R_04

C259

1u_6.3V_X5R_04

C173

1u_6.3V_X5R_04

C194

1u_6.3V_X5R_04

C283

10u_6.3V_X5R_06

4A

1.05V_LAN_M

C261

1u_6.3V_X5R_04

C183

1u_6.3V_X5R_04

C259

1u_6.3V_X5R_04

C173

1u_6.3V_X5R_04

C194

1u_6.3V_X5R_04

C283

10u_6.3V_X5R_06

C283

10u_6.3V_X5R_06

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10u_6.3V_X5R_06

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10u_6.3V_X5R_06

C283

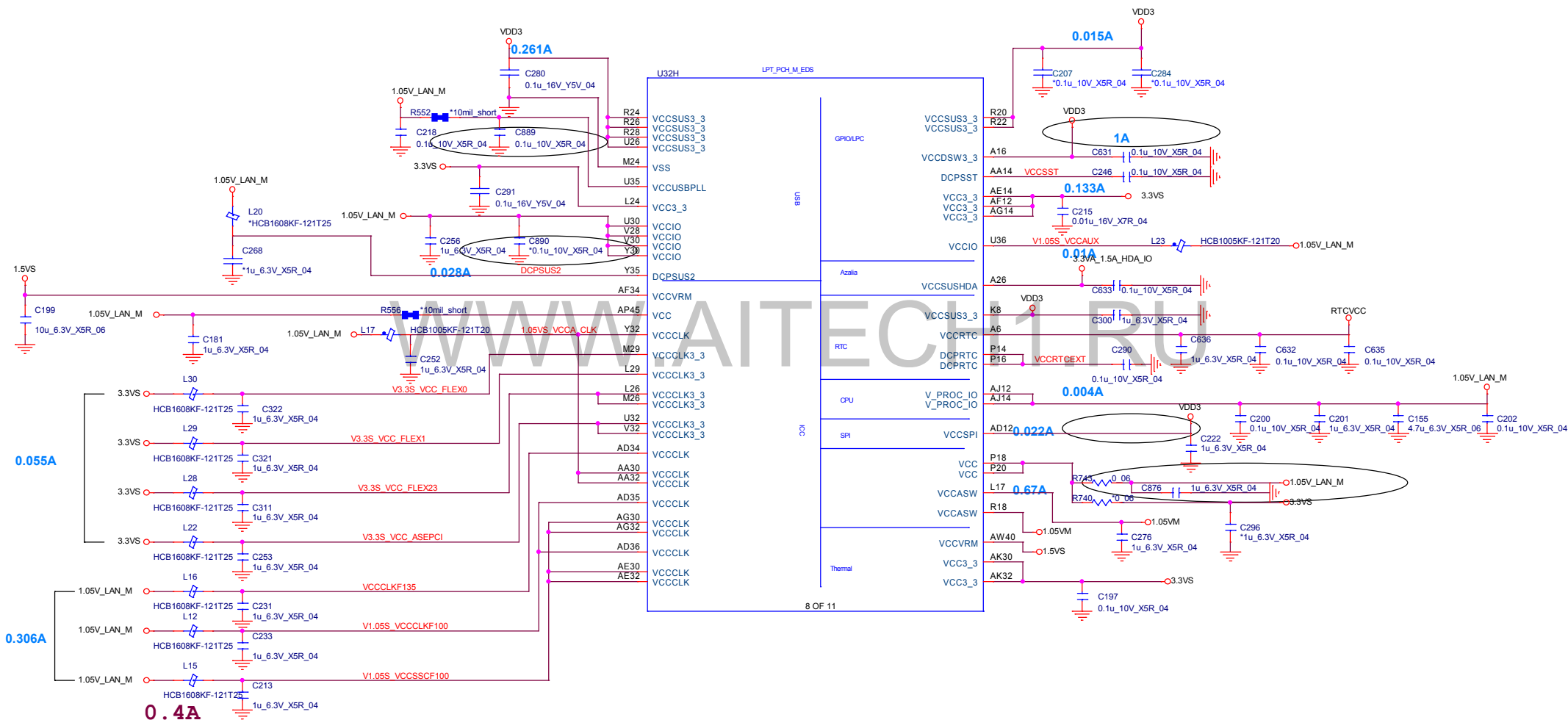
10u_6.3V_X5R_06

C283

10u_6.3V_X5R_06

C283

Lynx Point - M (POWER)

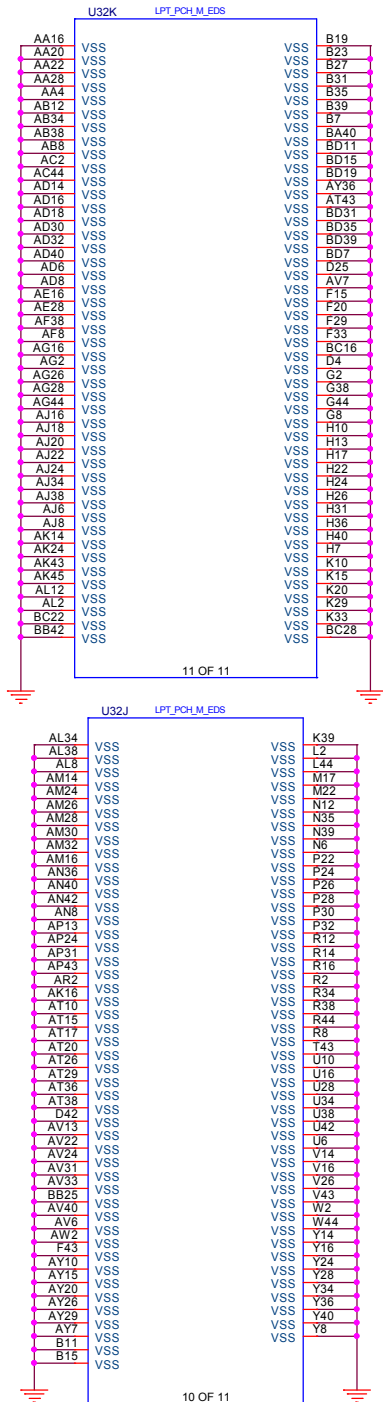


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[27] Lynx 8/9-POWER		
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Lynx Point -M (GND)

Lynx Point -M (CLK)

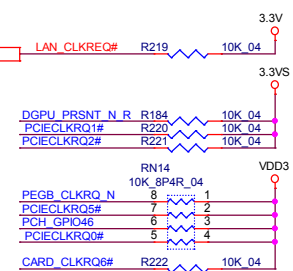
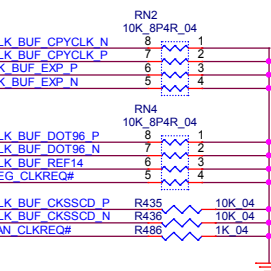
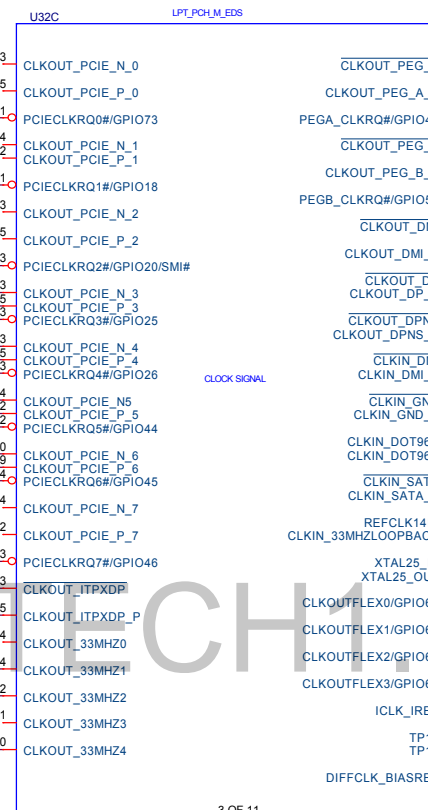


100MHz

100MHz

CLKOUT_33MHZ0~4
Breakin 300mils
Breakout 1500mils

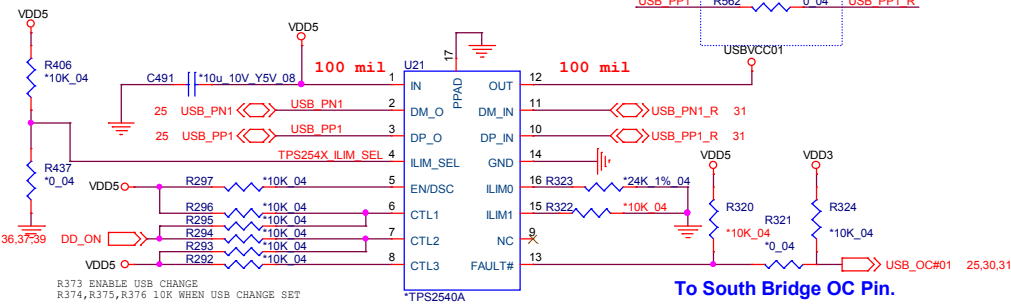
Voltage Rail	Voltage	S0 Iccmax	Current (A)
V_CPU IO	1.05	1	(mA)
V5REF	5	1	(mA)
V5REF Sus	5	1	(mA)
Vcc3_3	3.3	0.266	
VccADAC3	1.05	1	(mA)
VccADPLLA	1.05	0.08	
VccADPLLB	1.05	0.08	
VccCore	1.05	1.3	
VccDMI	1.1	0.042	
VccIO	1.05	2.925	
VccASW	1.05	1.01	
VccSPI	3.3	0.020	
VccDSW3_3	3.3	2	(mA)
VccDFTERM	1.8	0.19	
VccSus3_3	3.3	0.097	
VccSusHDA	3.3	1	(mA)
VccVRM	1.5	0.16	
VccCLKDMI	1.05	0.02	
VccSSC	1.05	0.095	
VccDIFFCLKN	1.05	0.055	
VccALVDS	3.3	1	(mA)
VccTX_LVDS	1.8	0.06	



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[28] Lynx 9/9-CLK/GND	
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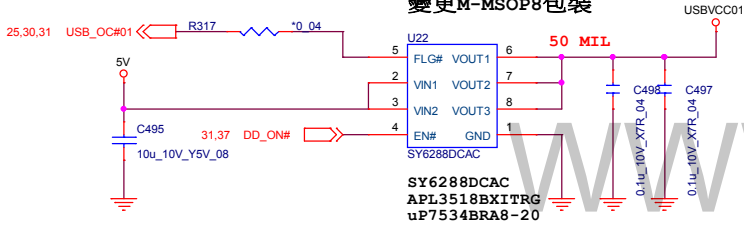
USB Charge PORT

Co-lay

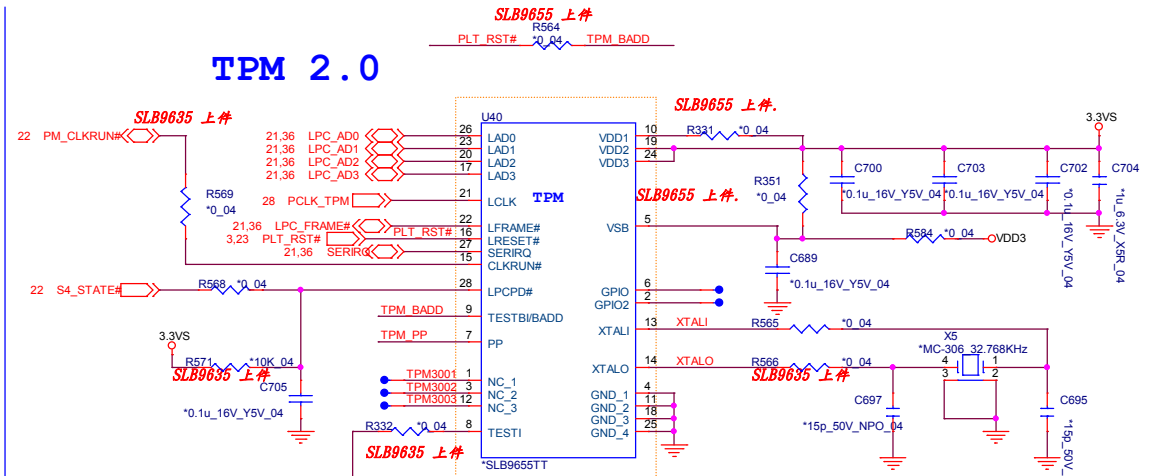


CTL1 CTL2 CTL3: 0 0 0----> Out discharge, power switch Off
 CTL1 CTL2 CTL3: 0 x 1----> Dedicated charging port, auto-detect
 CTL1 CTL2 CTL3: 1 0 1----> Dedicated charging port, Divider Mode only
 CTL1 CTL2 CTL3: 1 1 1----> Charging downstream port, BC1.2.

變更M-MSOP8包裝



TPM 2.0



Asserted before entering S3
 LPC reset timing:
 LPCPD# inactive to LRST# inactive 32~96us

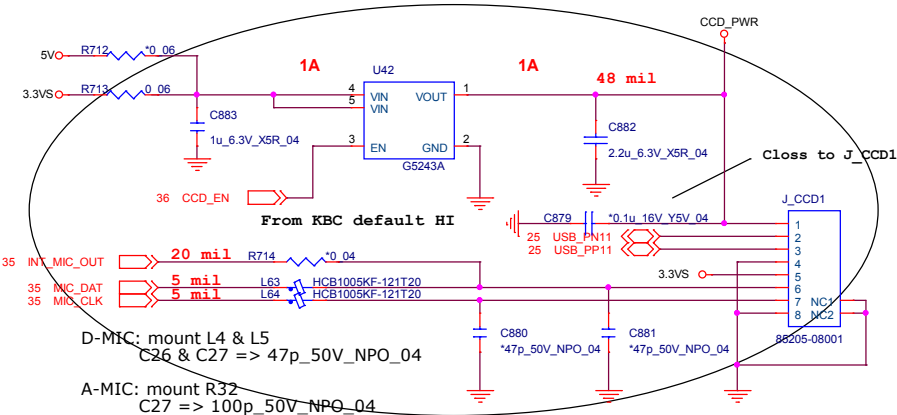
TPM_PP	HI: ACCESS LOW: NORMAL (Internal PD)
TPM_BADD	HI: 4E/ 4F H LOW: 2E/ 2F H



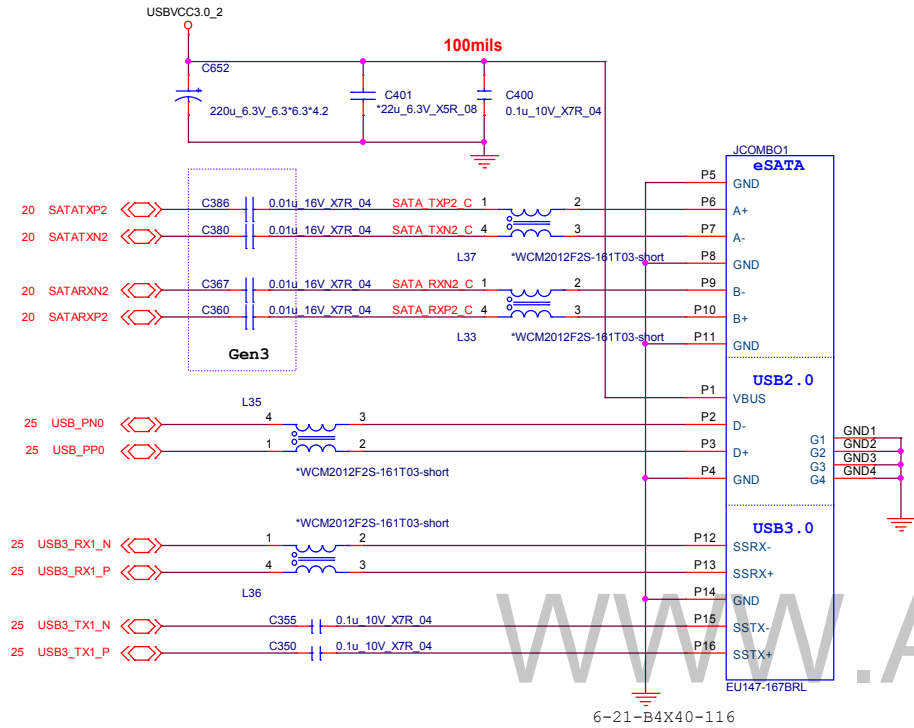
CCD+Mic

LID

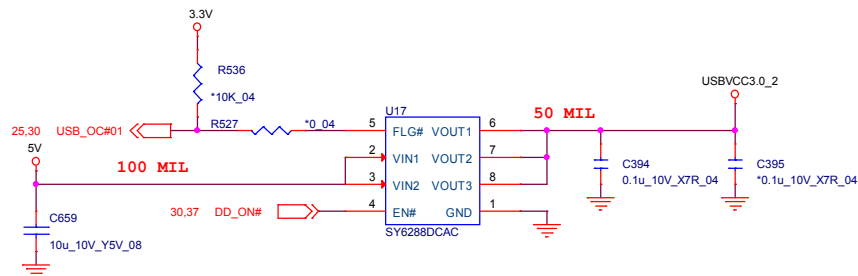
FOR POWER SWITCH BOARD



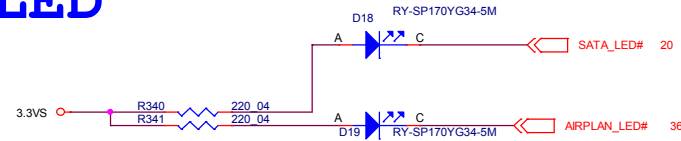
ESATA+USB3.0 USB3.0 PORT(PORT1)



USB POWER SWITCH



LED

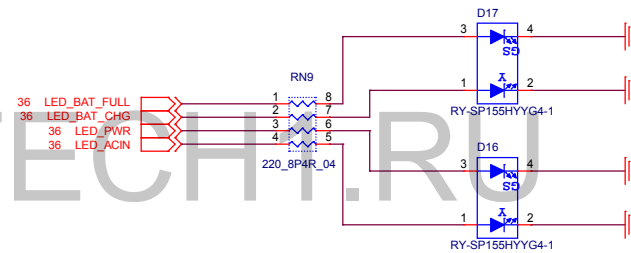


HDD LED

Airplane mode

WLAN/BLUETOOTH LED

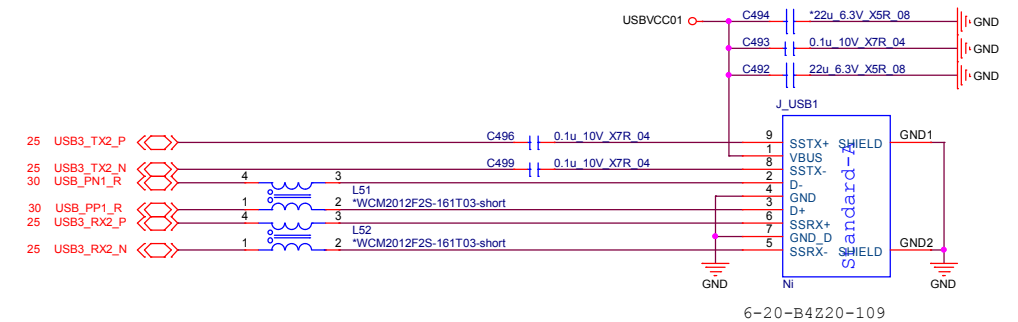
WLAN LED		
Windows 7	WLAN ON	亮
	WLAN OFF	不亮
Windows 8	Airplane ON	亮
	Airplane OFF	不亮



BAT LED

POWER ON LED

USB3.0 PORT (PORT3)



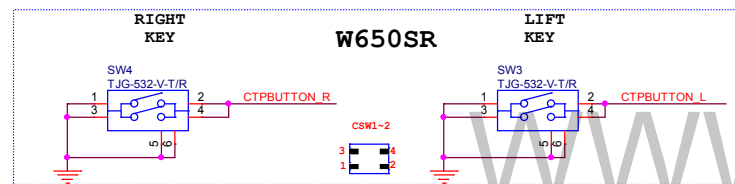
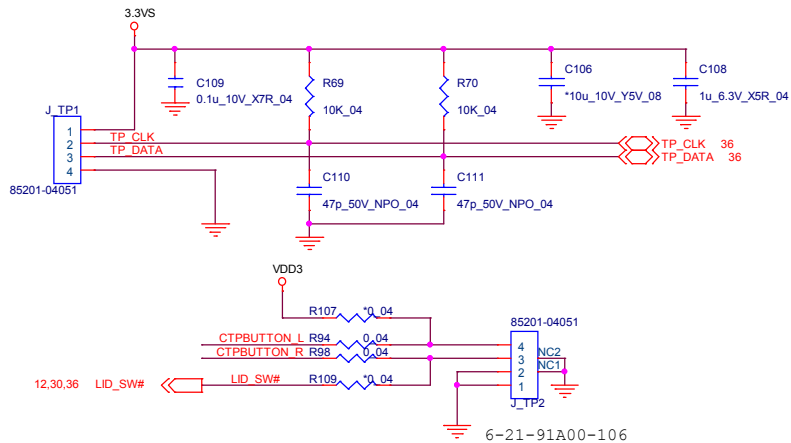
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[31] ESATA/USB3.0 CON, LED			
Size	Document Number	6-71-W65J0-D01	Rev 1.0
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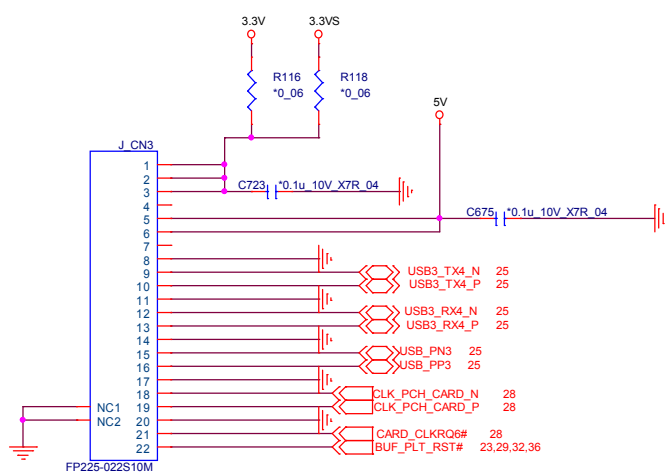
RTL8411 ESD request



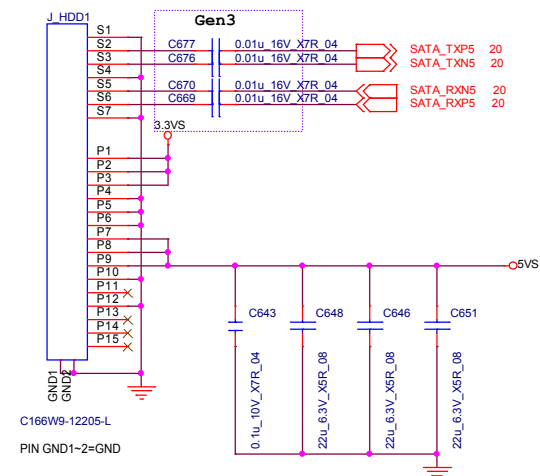
CLICK TP



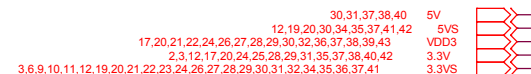
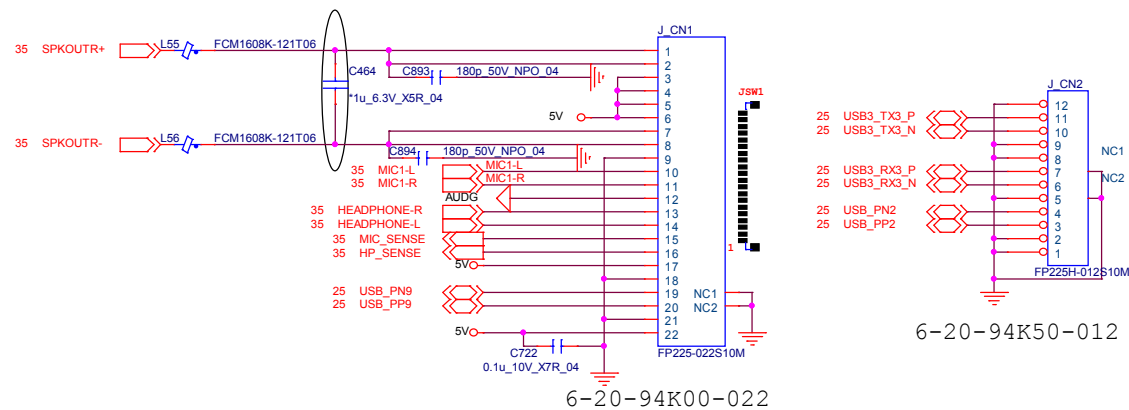
6-53-3050B-042 100g



HDD CONNECT1 (MASTER)



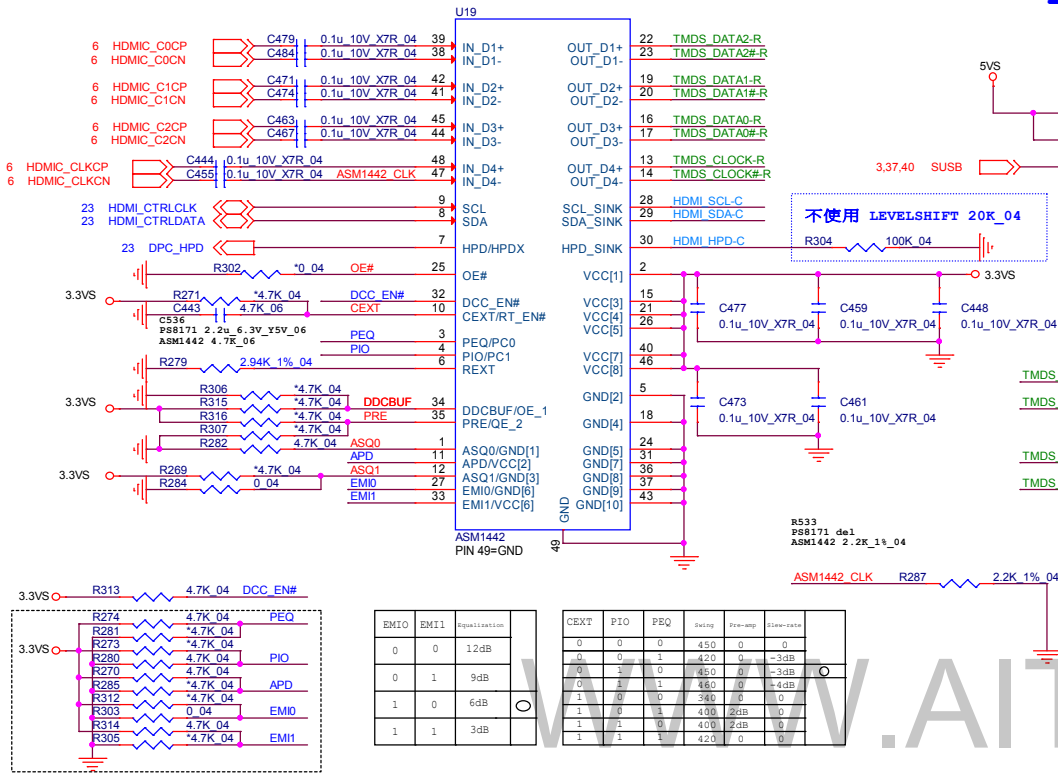
FOR AUDIO BOARD



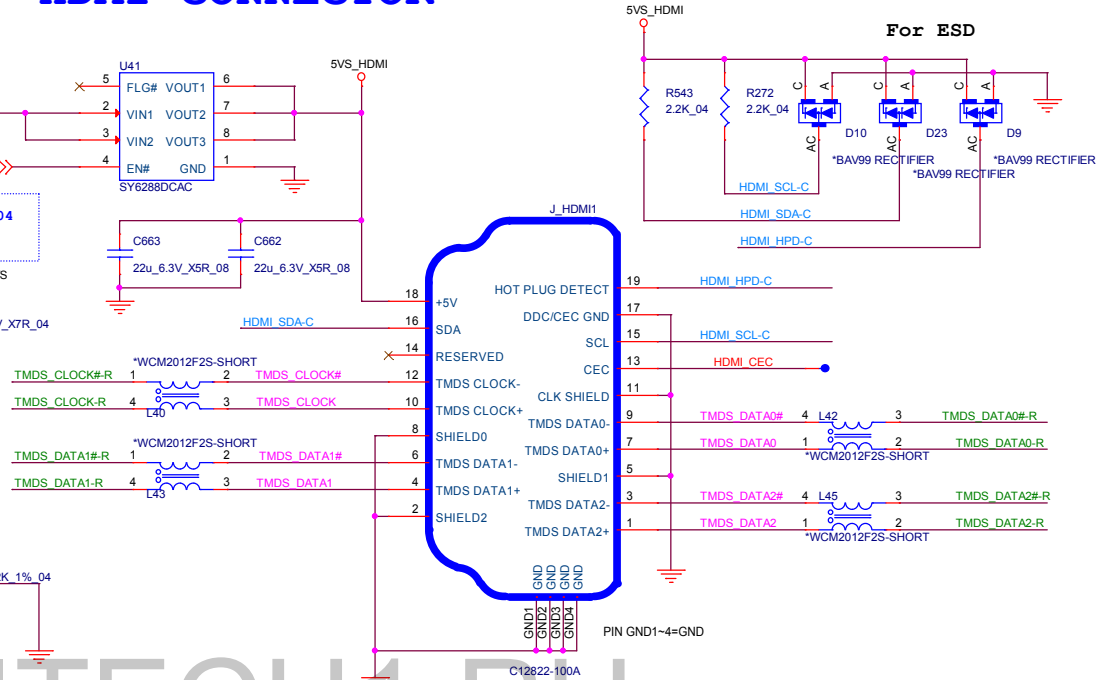
藍天電腦 CLEVO CO.

Title [33]HDD,CLICK TP,AUDIO CON,USB3.0 REDRIVER			
Size A3	Document Number 6-7P-W65J4-001	Rev 1.0	
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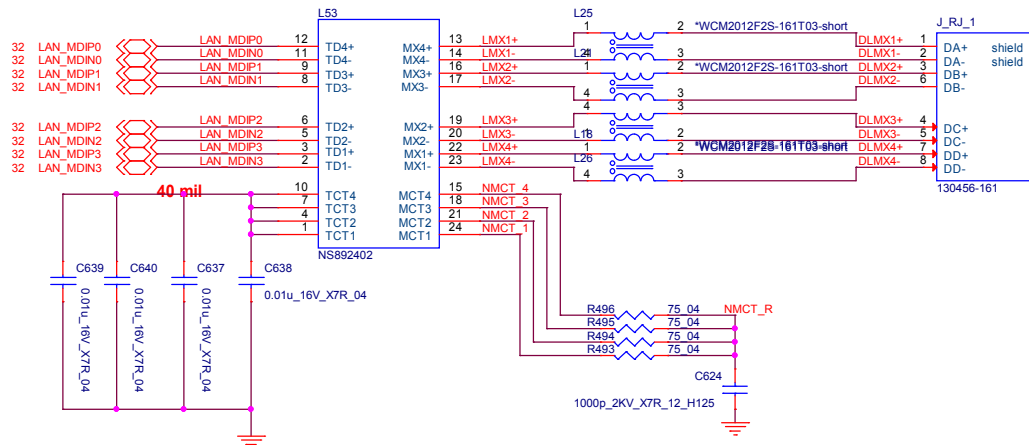
HDMI LEVEL SHIFT



HDMI CONNECTOR

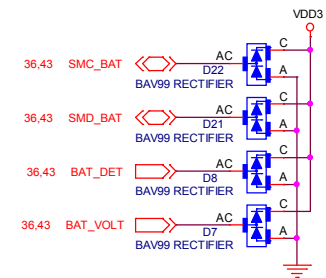
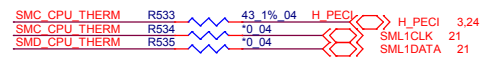
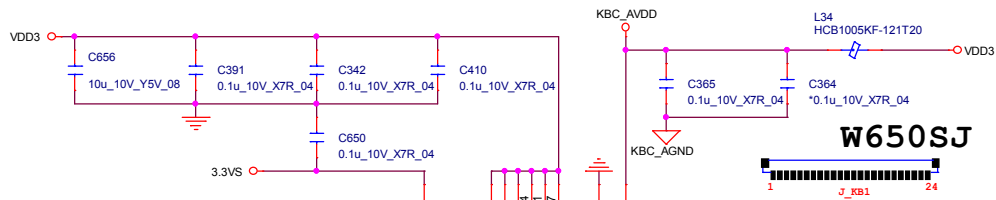


GIGA LAN (RTL8411) LAN POART

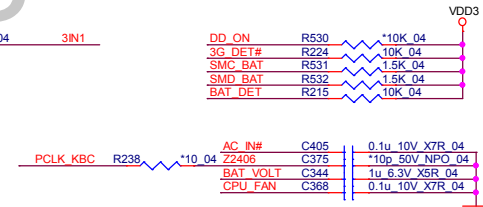
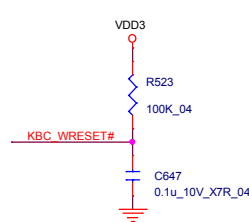
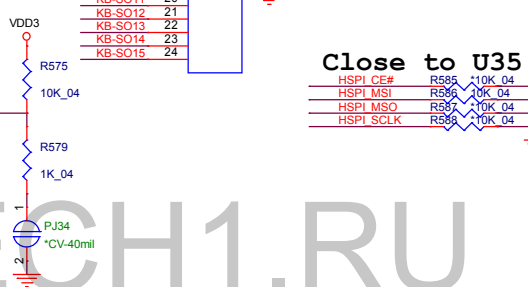
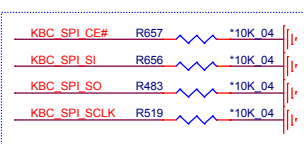
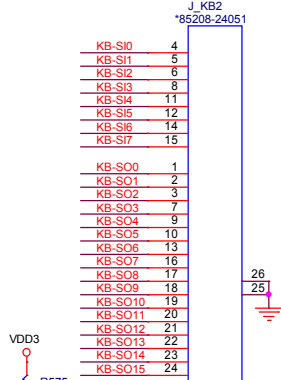
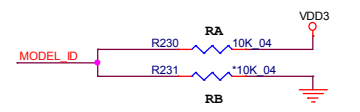


PD# Control AZ_RST# For 3.3V
HDA Link De-pop



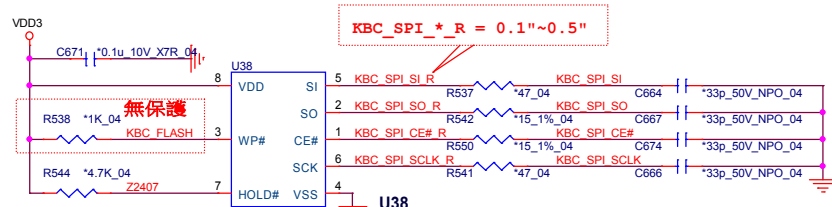


MODEL_ID	RA	RB	
V1.0	10K	X	W650SJ



PCB Footprint ACA-SPI-004-T03

KBC_SPI *_R = 0.1"~0.5"



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[36]KBC-ITE IT8587

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DD_ON#

SUSB

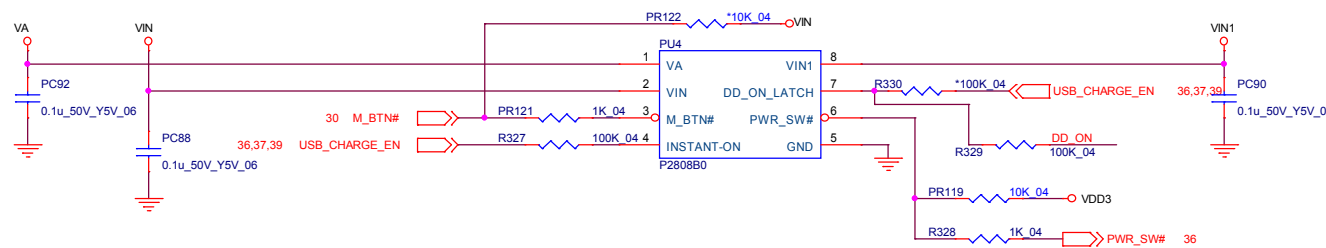
SLP_A

3.3VS

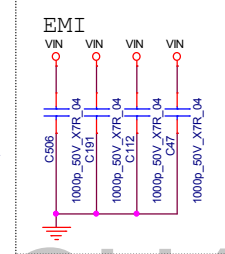
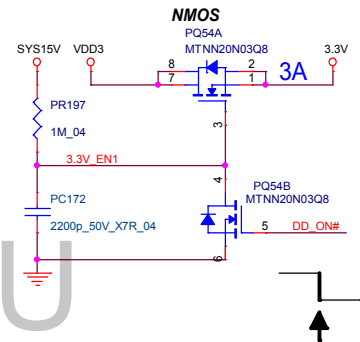
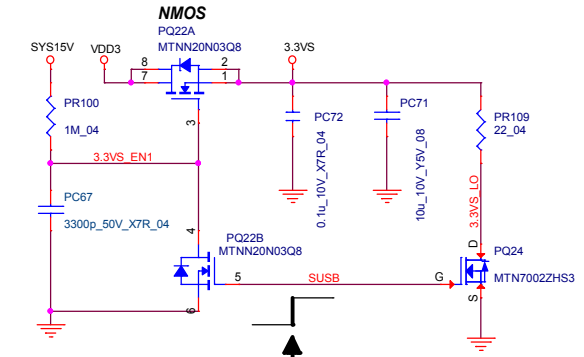
5V

5VS

3.3V



43	VA
39	VIN1
30,38,39	VDD5
38,39,42	SYS15V
30,31,33,38,40	5V
12,38,39,40,41,42,43	VIN
12,19,20,30,33,34,35,41,42	5VS
17,20,21,22,24,26,27,28,29,30,32,33,36,38,39,43	VDD3
2,3,12,17,20,24,25,28,29,31,33,35,38,40,42	3.3V
3,6,9,10,11,12,19,20,21,22,23,24,26,27,28,29,30,31,32,33,34,35,36,41	3.3VS



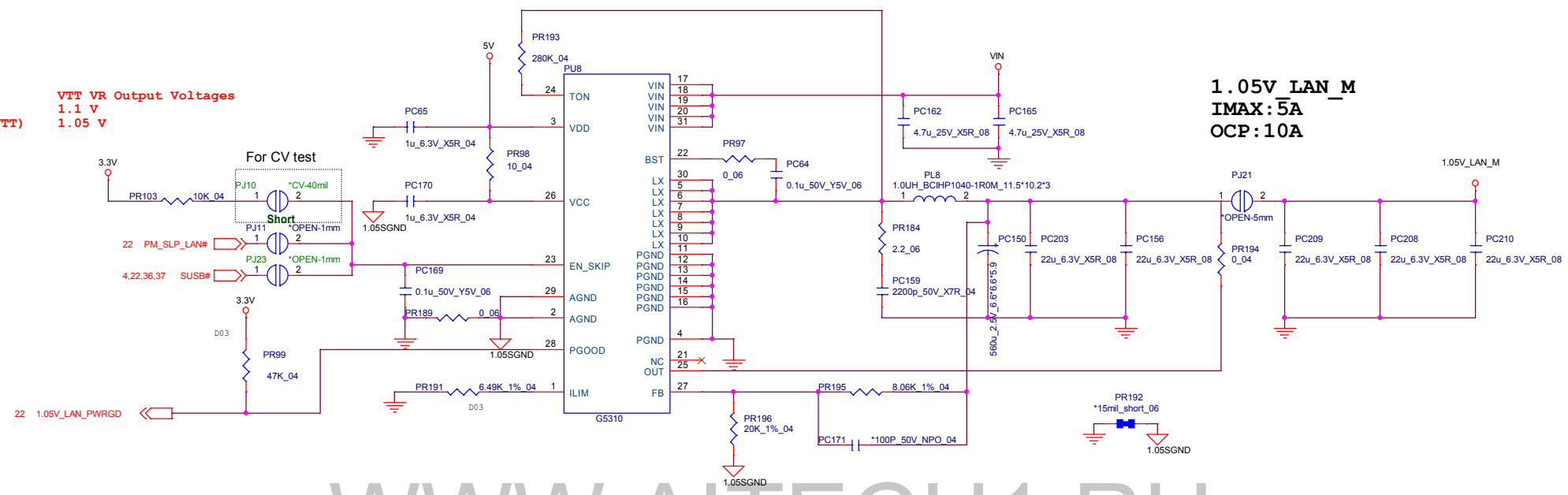
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Title		[37] 5VS,3VS,3.3VM,1.5VS_CPU	
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VTT_SELECT
low 1.1 V
high (V1.1S_VTT) 1.05 V

VTT VR Output Voltages
1.1 V
1.05 V

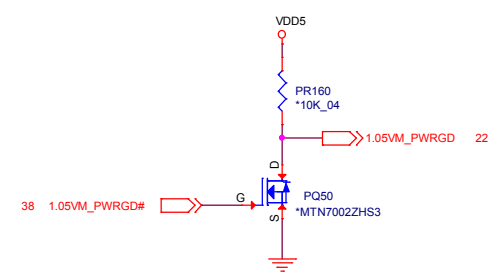
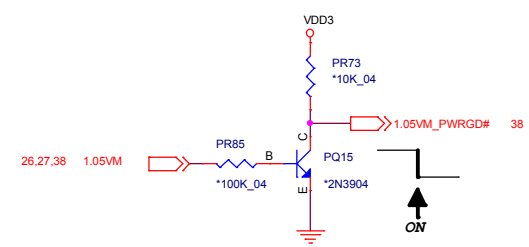
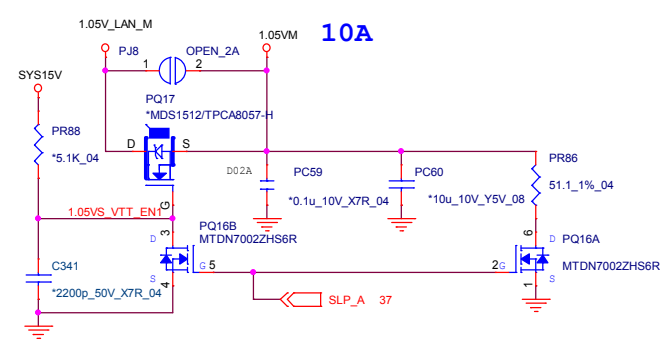
1.05V LAN_M
IMAX: 5A
OCP: 10A



WWW.AITECH1.RU

1.05VM

10A



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Title		[38]1.05V series	
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VDD3



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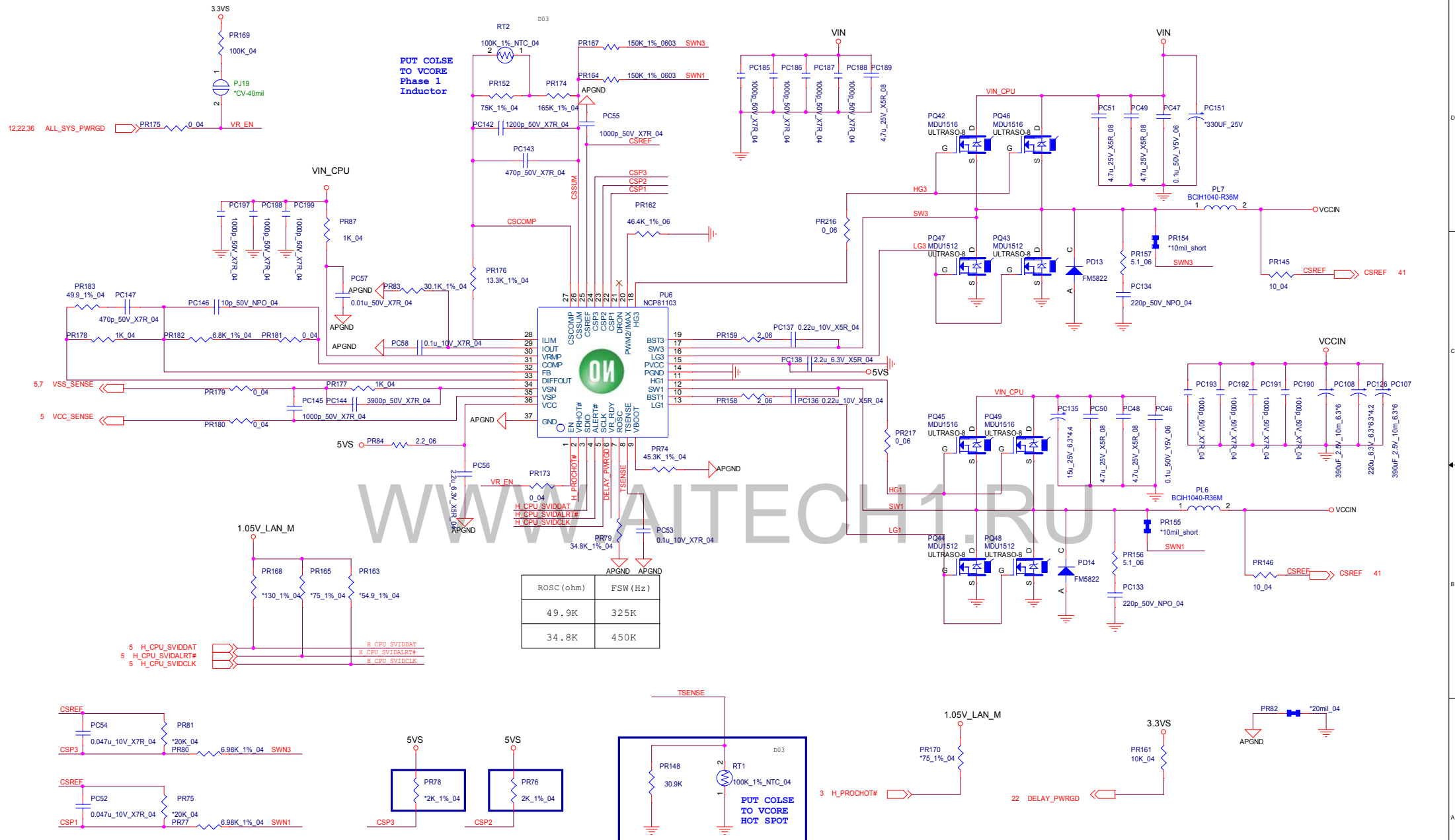
Rev
1.0

VDDQ_VTT

[illegible]

T _{iso}	[40]DDR 1.5V or 1.35V/0.75VS/1.5VS
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ROSC (ohm)	F'SW (Hz)
49.9K	325K
34.8K	450K

ASM for 1 phase ASM for 2 phase

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Title

[41]VCORE

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Custom

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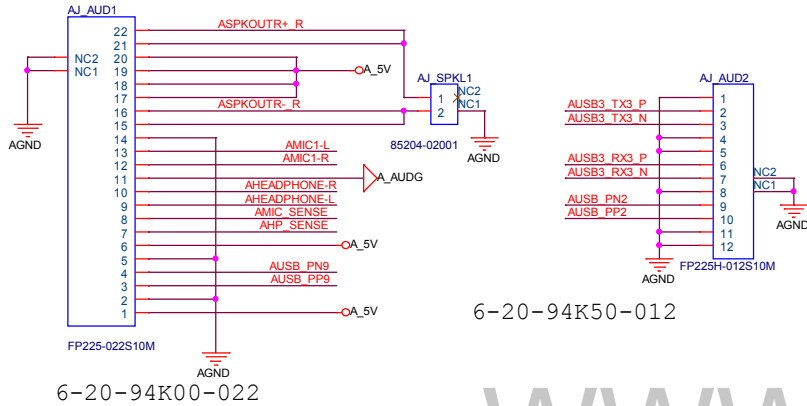
藍天電腦 CLEVO CO.			
Title [43]CHARGER,DC IN			
Size A3	Document Number	6-71-W65J0-D01	Rev 1.0
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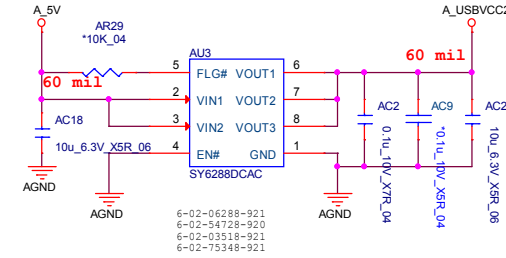


併成重疊

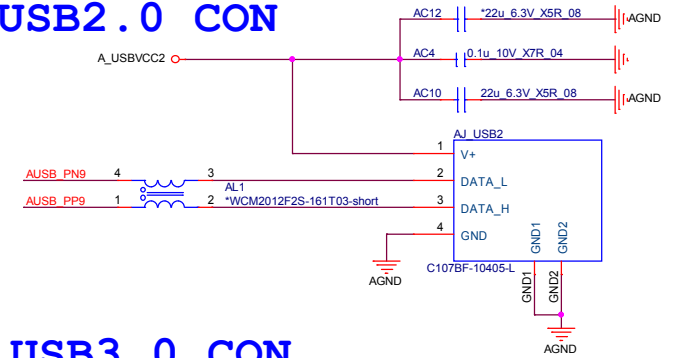
AUDIO BOARD Connect



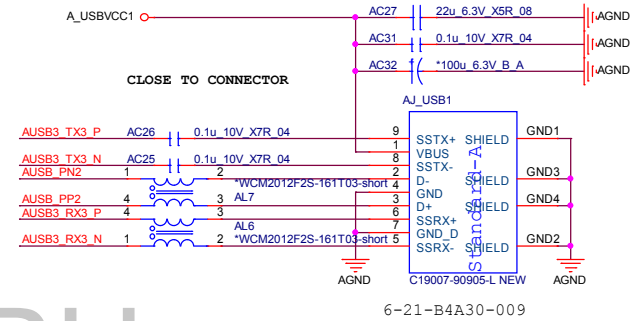
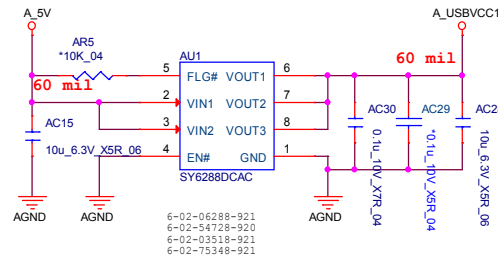
USB PORT (PORT9)



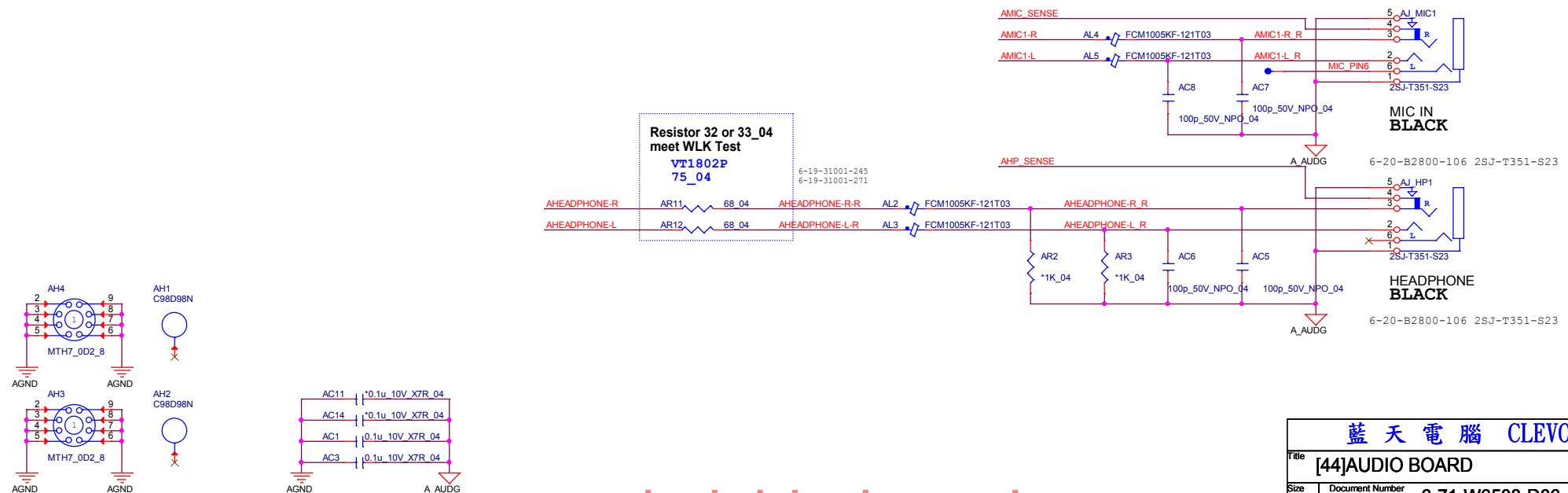
USB2.0 CON



USB3.0 CON



AUDIO JACK

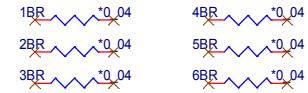


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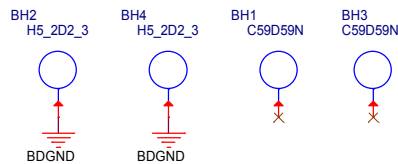
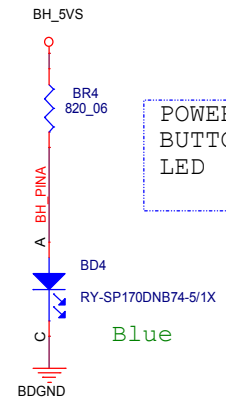
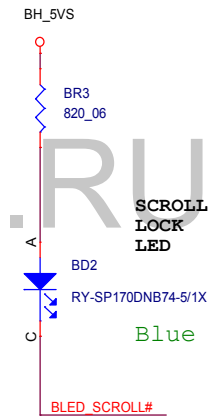
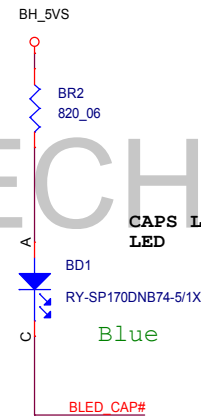
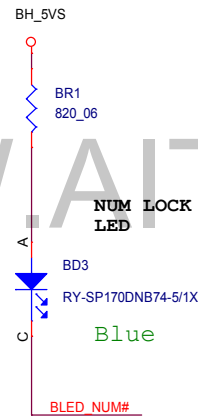
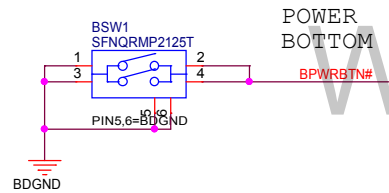
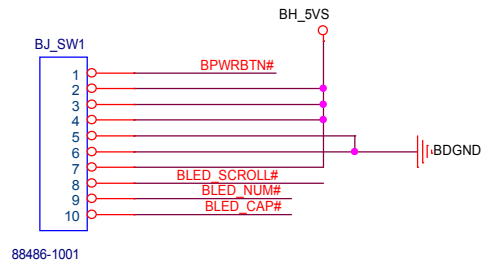
Title [44]AUDIO BOARD		
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POWER & LED BOARD

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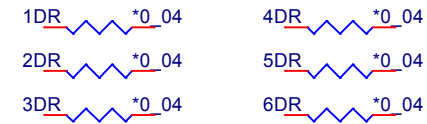
併成重疊



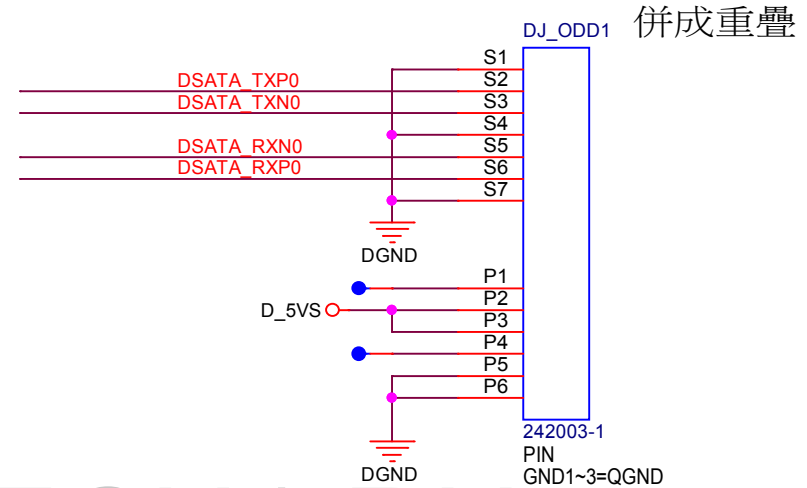
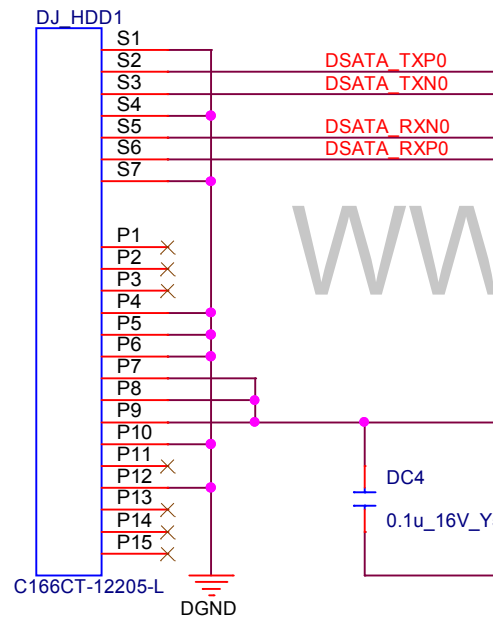
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Title [45]POWER SWITCH BOARD		
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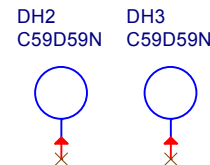
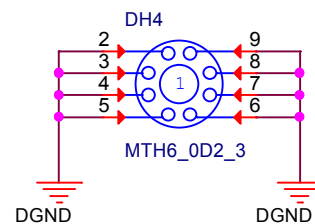
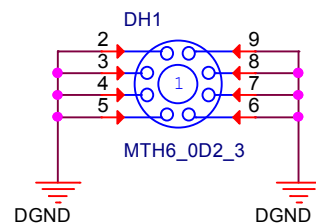
ODD TO HDD BOARD



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6-21-14030-013

6-21-C2710-122



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Title [46]ODD TO HDD BOARD

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